



2016 **61784-3-3—**

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CPF 3

(IEC 61784-3-3:2010, IDT)



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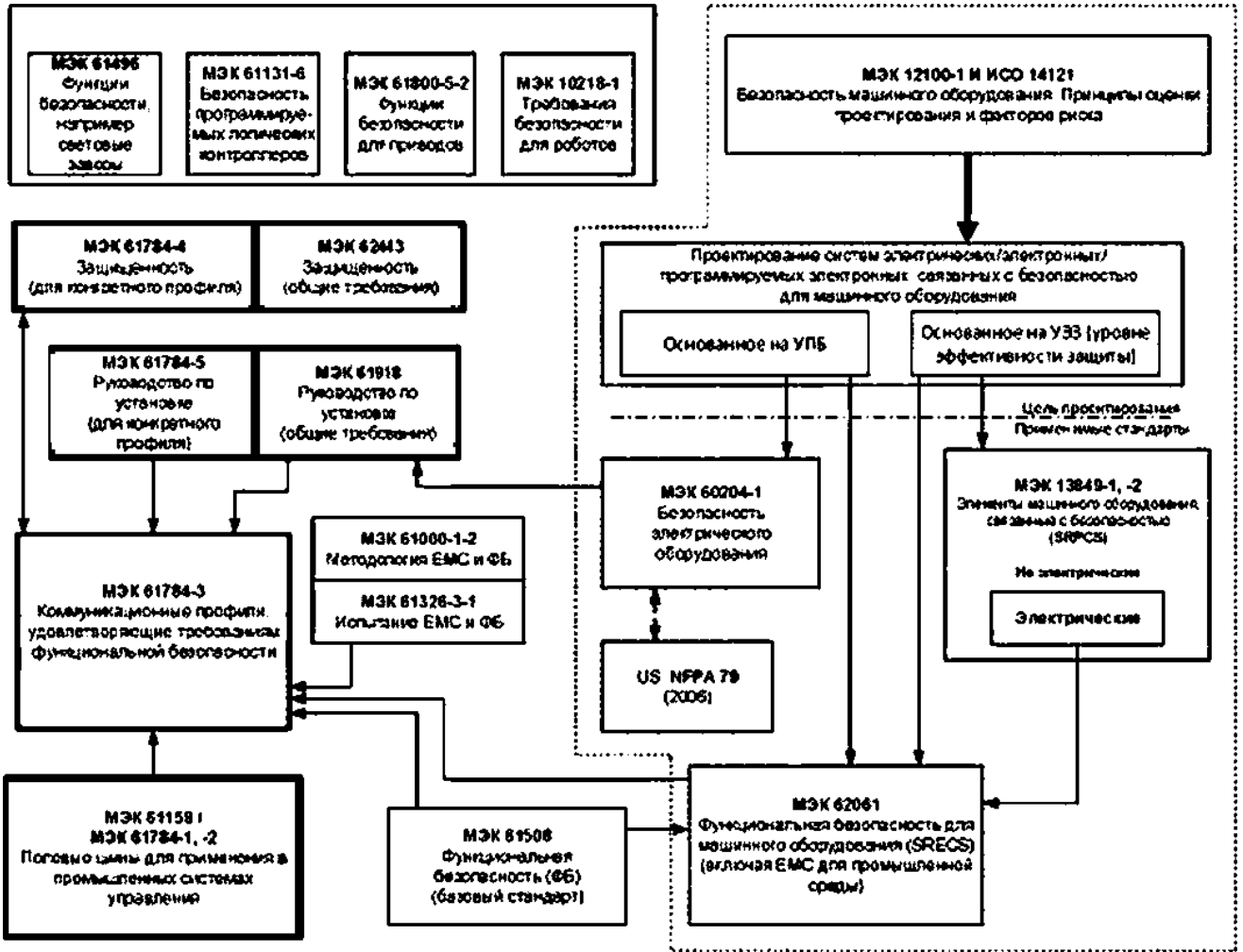
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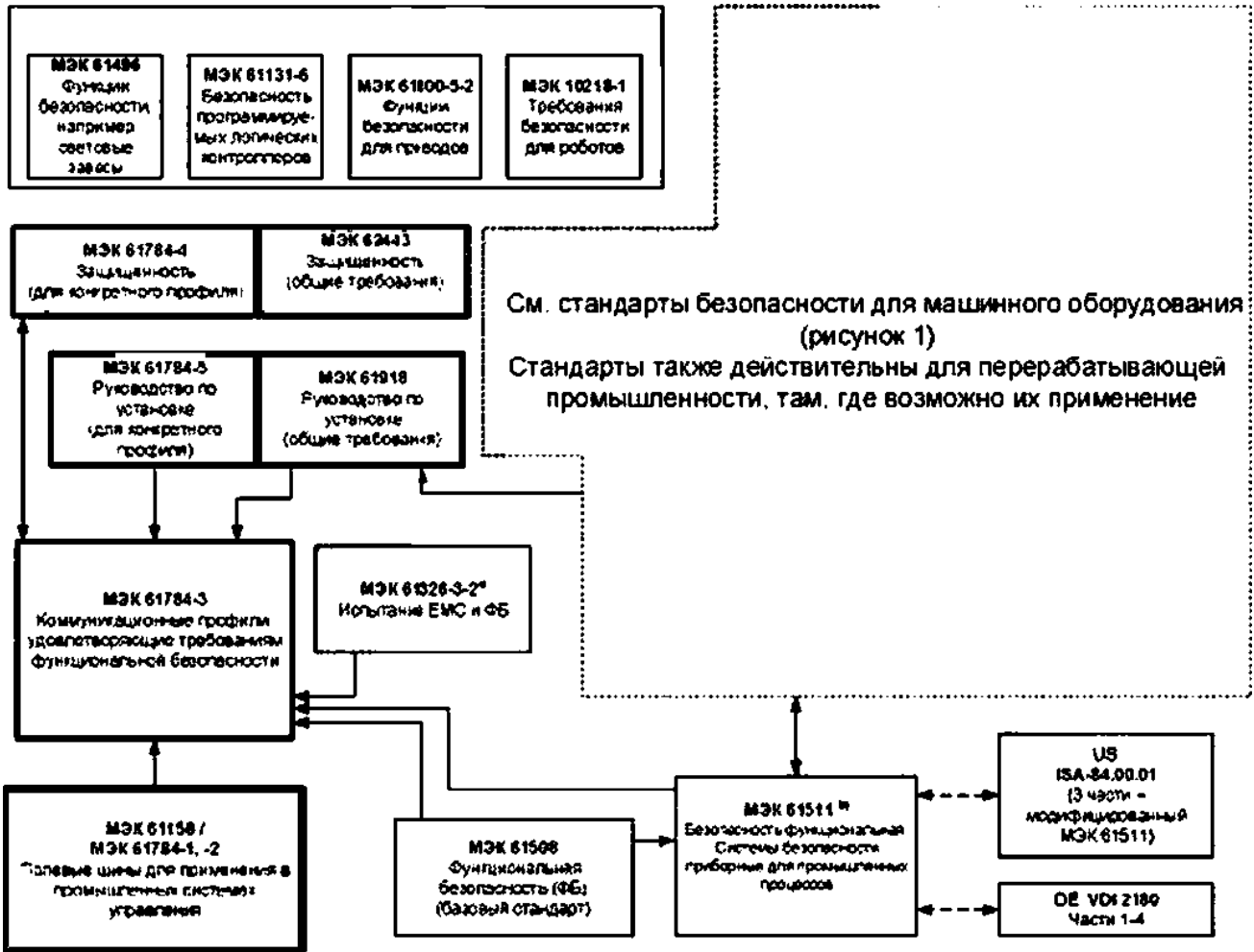
- 1267270- 2 (SI)
- WO00/045562-A1 [SI]
- WO99/049378-A1 (SI)
- 1686732 [SI]
- 1802019 [SI]
- 1921525- 1 (SI)

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CPF 3

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IEC 60204-1, Safety of machinery — Electrical equipment of machines — Part 1: General requirements
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IEC 61000-6-2, Electromagnetic compatibility (EMC) — Part 6-2: Generic standards — Immunity for industrial environments () 6-2.

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- IEC 61010-1. Safety requirements for electrical equipment for measurement, control, and laboratory use
Part 1: General requirements ()
- IEC 61131-2. Programmable controllers — Part 2: Equipment requirements and tests ()
- IEC 61131-3. Programmable controllers — Part 3: Programming languages ()
- IEC 61158-2. Industrial communication networks — Fieldbus specifications — Part 2: Physical layer specification and service definition ()
- IEC 61158-3-1. Industrial communication networks — Fieldbus specifications — Part 3-3: Datalink layer service definition — Type 3 elements ()
- IEC 61158-4-3. Industrial communication networks — Fieldbus specifications — Part 4-3: Datalink layer protocol specification — Type 3 elements ()
- IEC 61158-5-5. Industrial communication networks—Fieldbus specifications — Part 5-5: Application layer service definition — Type 5 elements ()
- IEC 61156-5-9. Industrial communication networks — Fieldbus specifications — Part 5-9: Application layer service definition — Type 10 elements ()
- IEC 61156-6-5. Industrial communication networks — Fieldbus specifications — Part 6-5: Application layer protocol specification — Type 3 elements ()
- IEC 61158-6-10. Industrial communication networks — Fieldbus specifications — Part 6-10: Application layer protocol specification — Type 10 elements ()
- IEC 61326-3-1. Electrical equipment for measurement, control and laboratory use — EMC requirements — Part 3-1: Immunity requirements for safety-related systems and for equipment intended to perform safety related functions (functional safety) — General industrial applications ()
- IEC 61326-3-2. Electrical equipment for measurement, control and laboratory use — EMC requirements Part 3 2: Immunity requirements for safety related systems and for equipment intended to perform safety related functions (functional safety) — Industrial applications with specified electromagnetic environment ()
- IEC 61508 (all parts). Functional safety of electrical/electronic/programmable electronic safety-related systems ()
- IEC 61508-2:2010. Functional safety of electrical/electronic/programmable electronic safety-related systems — Part 2: Requirements for electrical/electronic/programmable electronic safety related systems ()
- IEC 61511 (all parts). Functional safety — Safety instrumented systems for the process industry sector ()
- IEC 61784-1. Industrial communication networks — Profiles — Part 1: Fieldbus profiles ()
- (IEC 61764-2. Industrial communication networks — Profiles — Part 2: Additional fieldbus profiles for real-time networks based on ISOs IEC 8802-3 ()

- IEC 61784-3:2010. Industrial communication networks — Profiles — Part 3: Functional safety fieldbuses — General rules and profile definitions () 3.
- IEC 61784-5-3. Industrial communication networks — Profiles — Part 5: Installation of fieldbuses — Installation profiles for CPF 3 (CPF 3) 5.
- IEC 61918. Industrial communication networks — Installation of communication networks in industrial premises ()
- IEC 62061, Safety of machinery — Functional safety of safety-related electrical, electronic and programmable electronic control systems (/ / , -)
- IEC 62280-1:2002. Railway applications — Communication, signalling and processing systems — Part 1: Safety-related communication in closed transmission systems (1.)
- IEC 62280-2. Railway applications — Communication, signalling and processing systems — Part 2: Safety-related communication in open transmission systems (2.)
- IEC/TR 62390. Common automation device — Profile guideline (-)
- ISO13849-1. Safety of machinery — Safety-related parts of control systems — Part 1: General principles for design (1.)
- ISO 13849-2. Safety of machinery — Safety-related parts of control systems — Part 2: Validation (- 2.)
- ISO 15745-3. Industrial automation systems and integration — Open systems application integration framework — Part 3: Reference description for IEC 611 S8-based control systems (3. 61158)
- ISO 15745-4. Industrial automation systems and integration — Open systems application integration framework — Part 4: Reference description for Ethernet-based control systems (4. Ethernet)

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3.1.1

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(availability):

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(black channel):

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3.1.1.3

(communication channel):

3.1.1.4

(communication system):

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3.1.1.5

(connection):

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3.1.1.6

(Cyclic Redundancy Check. CRC):

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1 «CRC 1» «CRC 2» -
 2 (32). (33).
 3.1.1.7 (error): -
 [61508-4:2010]. (61156]
 / 1 / -
 / 2 / -
 3.1.1.6 (failure): < -
 — 61508-4
 (61508-4:2010.]. (/ 2382-14.01.11.] -
 — () -
 3.1.1.9 (fault): -
 — (IEV 191-05-01) " -
 (61506-4:2010.]. (/ 2382-14.01.10,] -
 3.1.1.10 (fidibus): , -
 3.1.1.11 (fieldbus system): , -
 3.1.1.12 (frame): DLPOU (-
).
 3.1.1.13 [frame check sequence (FCS)]: -
 , DLPOU () -
 ,
 1 FCS , . CRC -
 2 (32). (33).
 3.1.1.14 (hash function): () , -
 () () .
 1 -
 2 -
 CRC.
 (/TR 62210.] -
 3.1.1.15 (hazard): , -
 ,
 3.1.1.16 (master): , -
 ,
 3.1.1.17 (message): , -
 [/ 2382-16.02.01.]

3.1.1.18 (nuisance trip):

3.1.1.19 (proof test):

(61508-4 62061.) ; [performance level (PL)]:

(13849-1] 3.1.1.21 (protective extra-low-voltage. PELV):

30 8. 42.4 60

PELV SELV

(61131-2) 3.1.1.22 (redundancy):

[61508-4:2010.]-[/ 61508-4. 2382-14.01.12,] (reliability): (11.12).

8 1 2 « » « (MTBF) (MTTF)

[62059-11.] 3.1.1.24 (risk):

(61508-4:2010). [/ 61508-5:2010. 51:1999. 3.2) (safety communication layer, SCL):

3.1.1.26 61508. (safety connection):

3.1.1.27 (safety data):

3.1.1.28 (safety device): 61508

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3.1.1.29 (safety extra-low-voltage. SELV)SELV): -
 , 30 . 42.4 60 8 -
 — SELV -

[61131-2)
 3.1.1.30 (safety function): , / / (, -
 ,) , , -
 — 61506-4 , .

(61508-4:2010.]
 3.1.1.31 (safety function response time): -
 , , -
 — 61784-3:2010. 5.2.4 -

3.1.1.32 ; [safety integrity level (SIL)]; -
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1 (. 61506-4:2010. . 3.5.17)
 61508-1:2010. 2 3. -
 2 , // , . -
 3 () , * (= 1.2.3 4) -
 : « , , * (= 1.2.3 4) -

(61508-4:2010]
 3.1.1.33 (safety measure): -
 v 61508. -
 1 , , -
 2 61764-3:2010. -

5.3 5.4.
 3.1.1.34 (safety-related application): , -
 61508 -
 3.1.1.35 (safety-related system): , -
 61508. -
 3.1.1.36 (slave): , -

3.1.1.37 (spurious trip): , -

3.1.2 CPF 3.
 3.1.2.1 (bit): . -
 3.1.2.2 (codename): -
 3.1.2.3 (configuration): . -

	F-	—	<	,	,			
3.1.2.4			(consecutive number):					
			PDU					
1			61784-3.					
2			POU		(-VI)	
3.1.2.5			CRC(/2).					
			-CPD (CPD-Tool):					
3.1.2.6			(cycle):					
3.1.2.7			[device access point (DAP)]:					
			()					
3.1.2.8			[device acknowledgement time (DAT)]:					
	F-		PDU					
			PDU					
3.1.2.9			(driver):					
3.1.2.10			F [fail-safe (F)]:					
3.1.2.11			FV [fail-safe values (FV)]:					
			(FV)					
			«0».					
3.1.2.12			(fail-safe state):					
			(
)					
3.1.2.13	F-		(F-Device):				CP 3/RTE.	
			FSCP 3/1			F-		
3.1.2.14	F-		(F-Driver):				PDU	
	F-					FSCP 3/1.		
3.1.2.15	F-		(F-Host):				FSCP 3/1	
			PLC IPC					
3.1.2.16	F-		(F-Module):				FSCP 3/1.	
	F-							
	F-							
3.1.2.17	F-		(F-Slave):				3/1	3/2,
			FSCP 3/1.			F-		
3.1.2.18			(fa jtit reaction):					

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	F-	:	—	,	/	,	.		
	F-CPU:							F-1/	-
	F-	:		,		F-	.		-
3.1.2.19				,	[function block (FB)J:		,		-
3.1.2.20	F-			,	[host acknowledgement time (HAT));				-
			PDU						
					PDJ				
					/				
3.1.2.21				,				(-Controller):	-
								CP 3/RTE	-
			—	<	3/1			1	
3.1.2.22				,				(-Device):	-
	CP 3/RTE,								
			—	<	3/1				
3.1.2.23				,				(IO-Module):	-
3.1.2.24				,				(-Supervisor):	-
			—						
3.1.2.25				,				(-System):	-
3.1.2.26	F-		((iParameter):				
			—		i				
3.1.2.27			inap-cepsep		(iPai-Server):			F-	
	F-								
3.1.2.28				,	1 [master (class 1):				-
			3/1.						
3.1.2.29				,	[process values (PV):			(PDU	-
)						
3.1.2.30					(Qualifier):				
3.1.2.31				,	I/O (shared I/O):				
			—	3/RTE	I/O.	FSCP 3/1.			
3.1.2.32					(toggle bit):				-
			(F-	F-		
3.1.2.33				,	USB [universal serial bus (USB):				
					480 /				
			—	USB					

3.1.2.34 -VI (V1-mode): FSCP 3/1 {48}.
 3.1.2.35 - 2 (V2-mode) FSCP 3/1
 3.1.2.36 VLAN- (VLAN tag): Ethernet
 VLAN-ID.

3.2

3.2.1

		[61784-1]
CPF		[61784-1]
CRC		
OLL		[/ 7498-1]
DLPDU		
		[61508-4:2010]
//	/ /	[61508-4:2010]
FAL	(Fieldbus Application Layer)	(61158-5)
FCS		
FSCP	,	-
HD		
MTBF		
MTTF		
PDU		[/ 7498-1]
PELV		
PFD	-	[61508-6:2010]
PFH	<h')	[61508-6:2010]
PhL		[/ 7498-1]
PL		(13849-1)
PLC		
SCL		
SELV		
SFRT		
		[61506-4:2010]

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3.2.2 CPF 3.

SIS —

(safety instrumented systems)

API		
ASE		
ASIC		
3/1	PROFIBUS DP	
3/2	PROFIBUS	
CP 3/RTE	PROFINET	
DAP		
F	(,)	
-		
FV		
GSD	(,)	
GSDL	(3/1 3/2)	
GSDML	(3/RTE)	
HAT		
I/O	-	
PN 10	PROFINET = 3/4 3/6	
PV		
RADIUS		
	()	
SSID		
UML		[57]
USB		[62]
VLAN		
WCDT		
WD-		
WPA2	Wi-Fi 2	[28]
XML		[59]. [60]. [61]

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UML2

[57].

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/RTE

PROFINET

4 FSCP 3/1 (PROFIsafe™)

(КахPROFIBUS™. PROFINET™2*)

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61158-5-10.

61158-6-3.

61158-6-10.

61158-2

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61158-3-3,

61158-4-3.

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61784-1.

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61784-2.

FSCP 3/1 (PROFIBUS™. PROFINET™*)

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FSCP 3/1

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FSCP 3/1

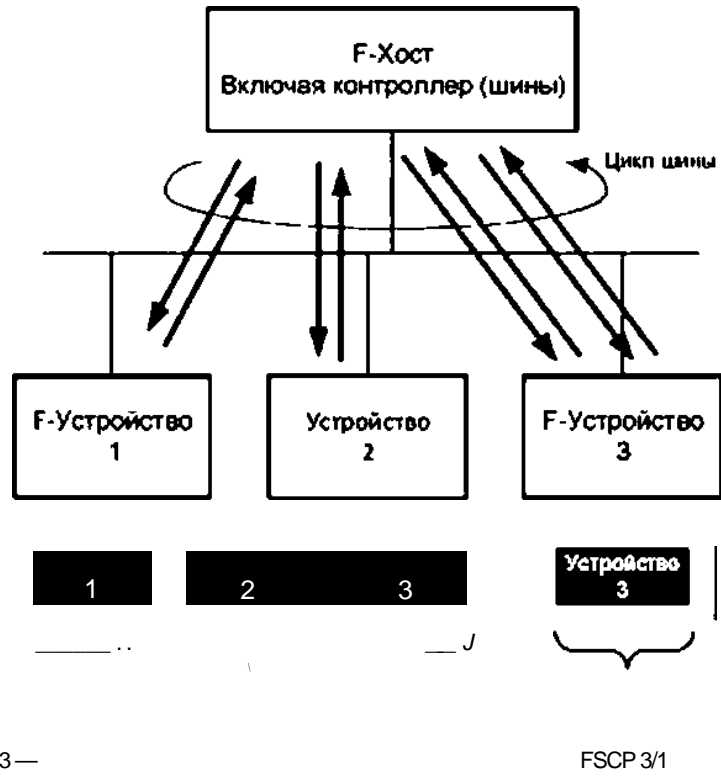
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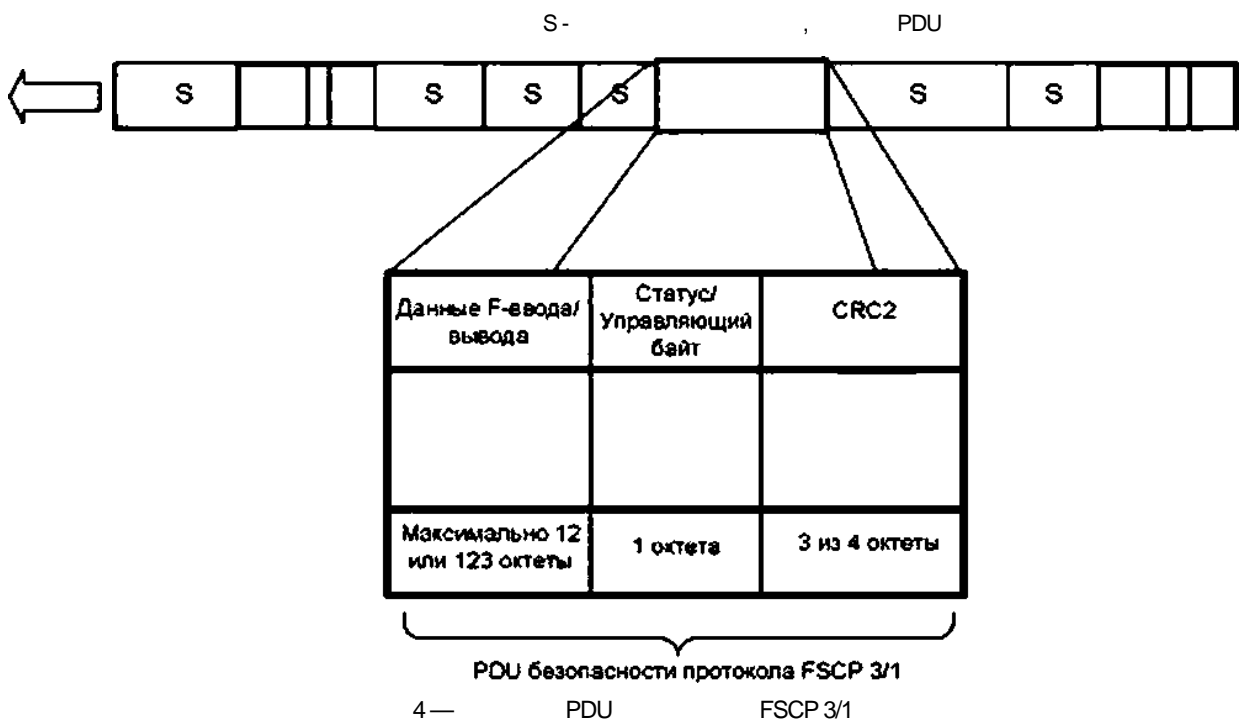
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PNO.

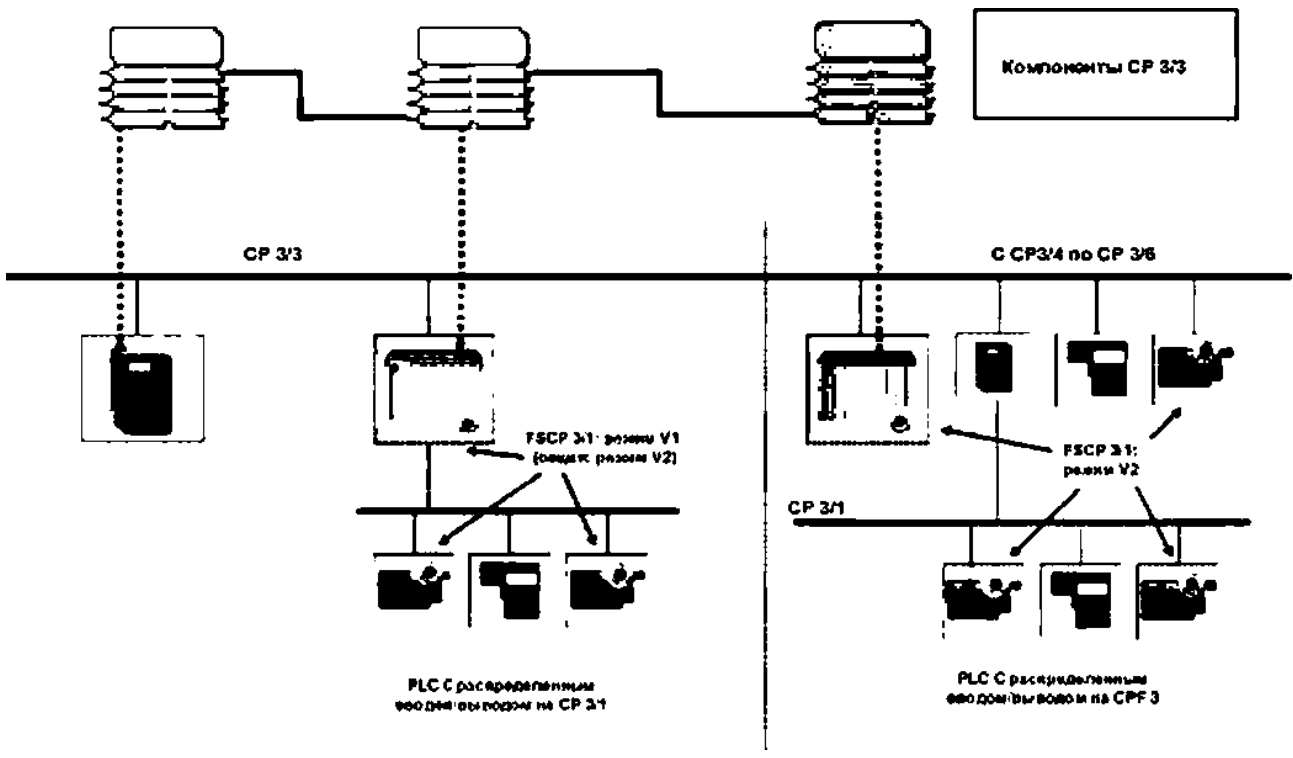
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PDU
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CRC
CRC2 (4).



FSCP 3/1 : V1 V2. V1
 Ethernet/CP 3/RTE. 3/1. « »
 FSCP 3/1. V2
 3/4 V2. V1 3/1. 3/1.
 (3/3) V2. 5 FSCP 3/1 3/1 PROFINET 3/RTE.
 PROFIBUS (3/1 3/2) Ethernet,
 PROFINET (3/RTE), « »
 FSCP 3/1, 5.1 5.2 5.3 3/RTE
 3/1 FSCP 3/1 3/2 5.4. 5.5
 5.5.4. 6.1 6.3 F-



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7 PDU (7.1),
 F- 2 (7.2.2 7.2.4).
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F-	7.4.	(8.1)	-
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F-		8.5	-
		F-	
		8.6.	-
9.		F-	-
	10.1.	10.2.	-
	CRC		-
	FSCP 3/1	((44). [45]).	-
5			
5.1			
	2.		
FSCP 3/1	GS-ET-26 [31].		
5.2	NE97 [58].		
a)		FSCP 3/1.	
b)			
(61508).	4 (EN 954-1 [25]).	PLe (13849-1).	
c)			
d)			
e)	(F- F- —F-yc / F-1/ -).		1:1.
f)			
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h)		ASIC	
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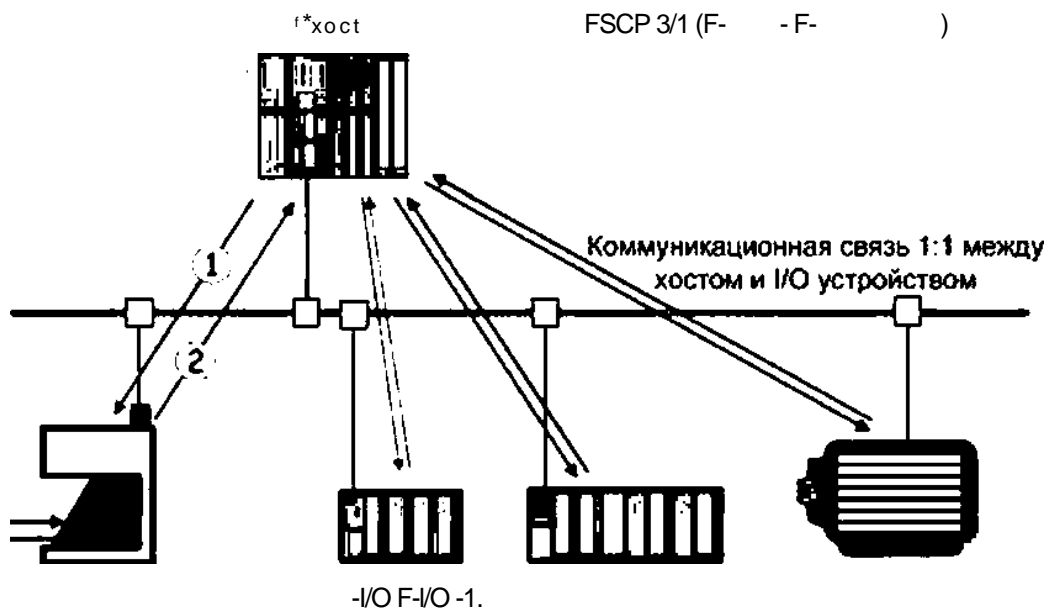
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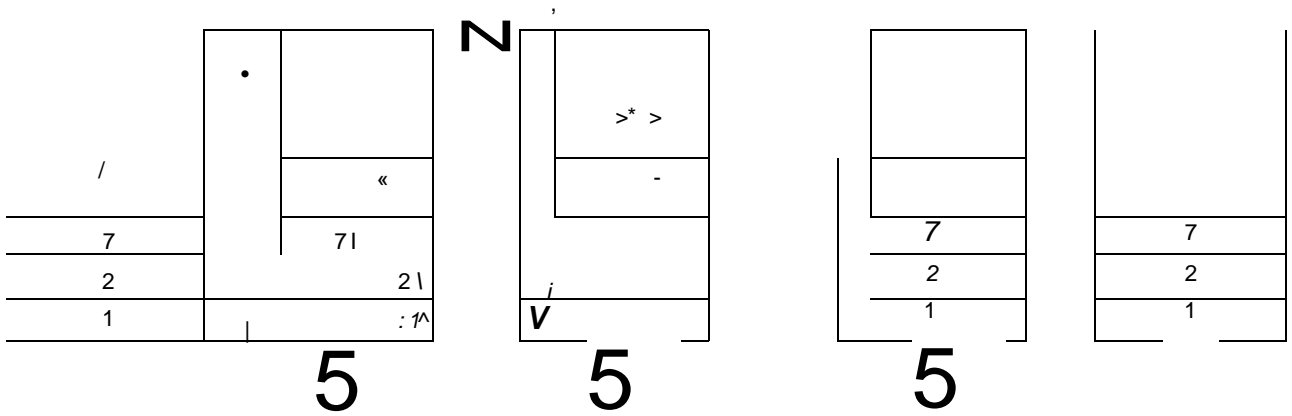
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5.4.2 CPF 3

CP 3/RTE

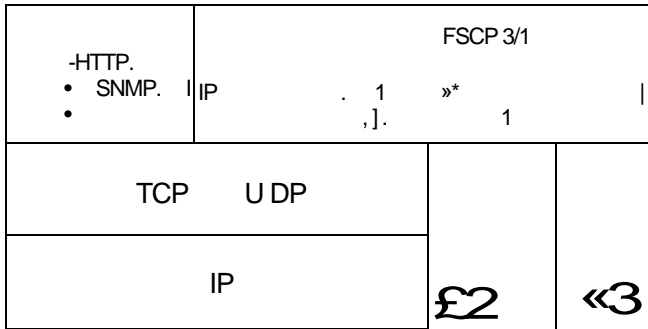
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TCP/IP UDP.

TCP/UDP/IP



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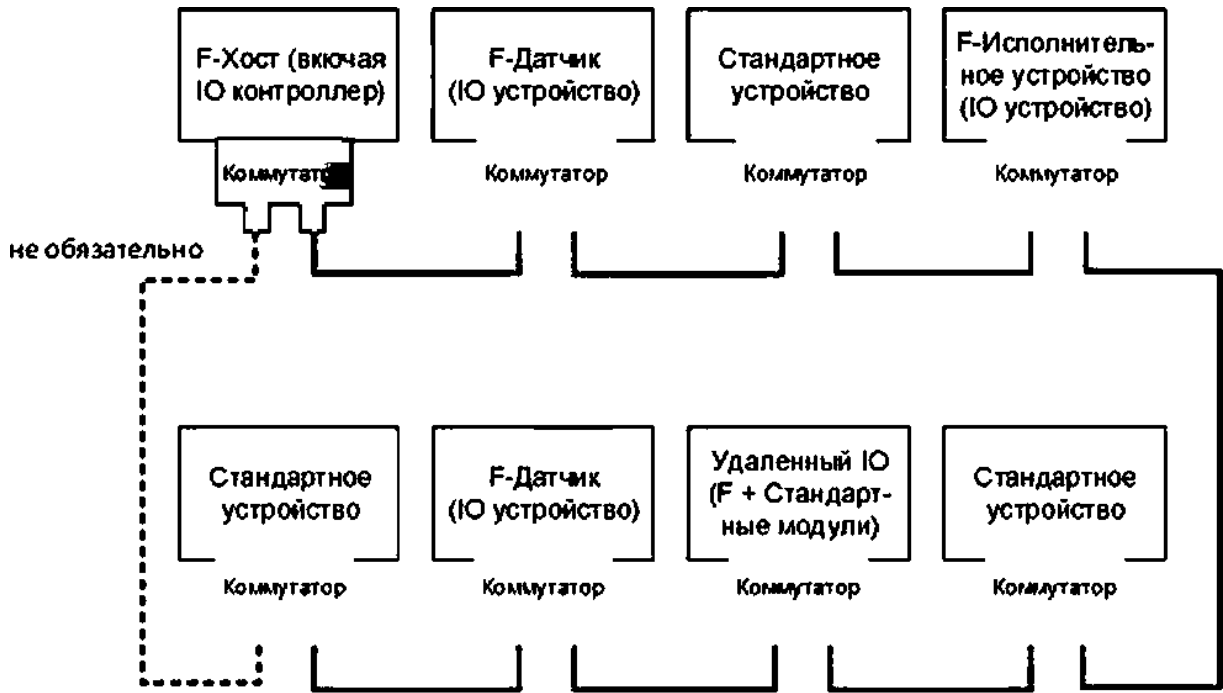
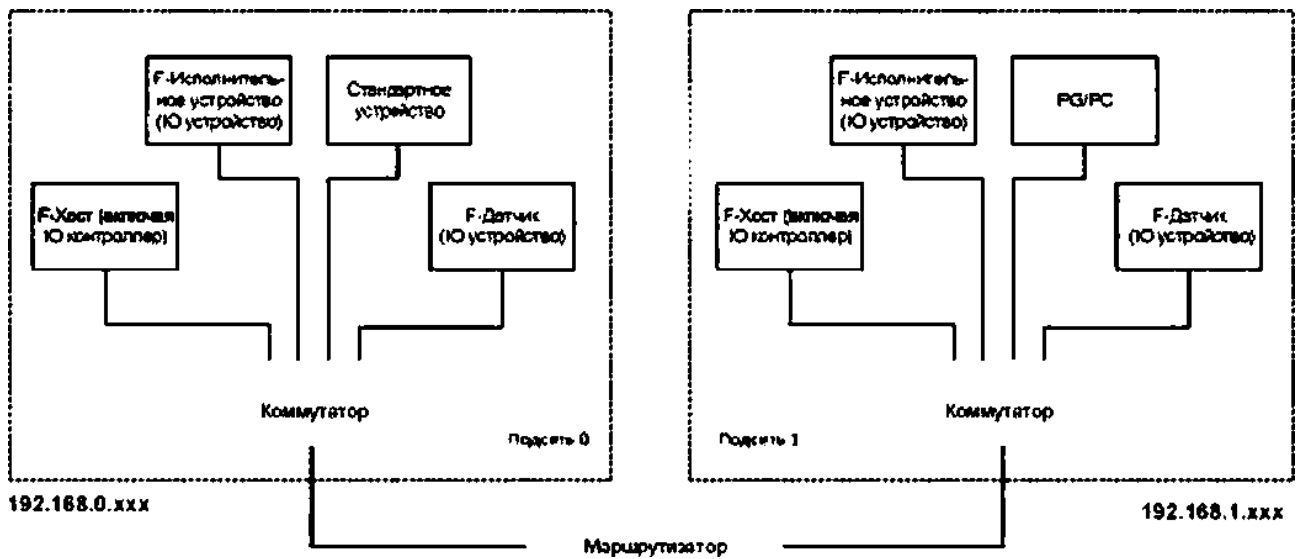
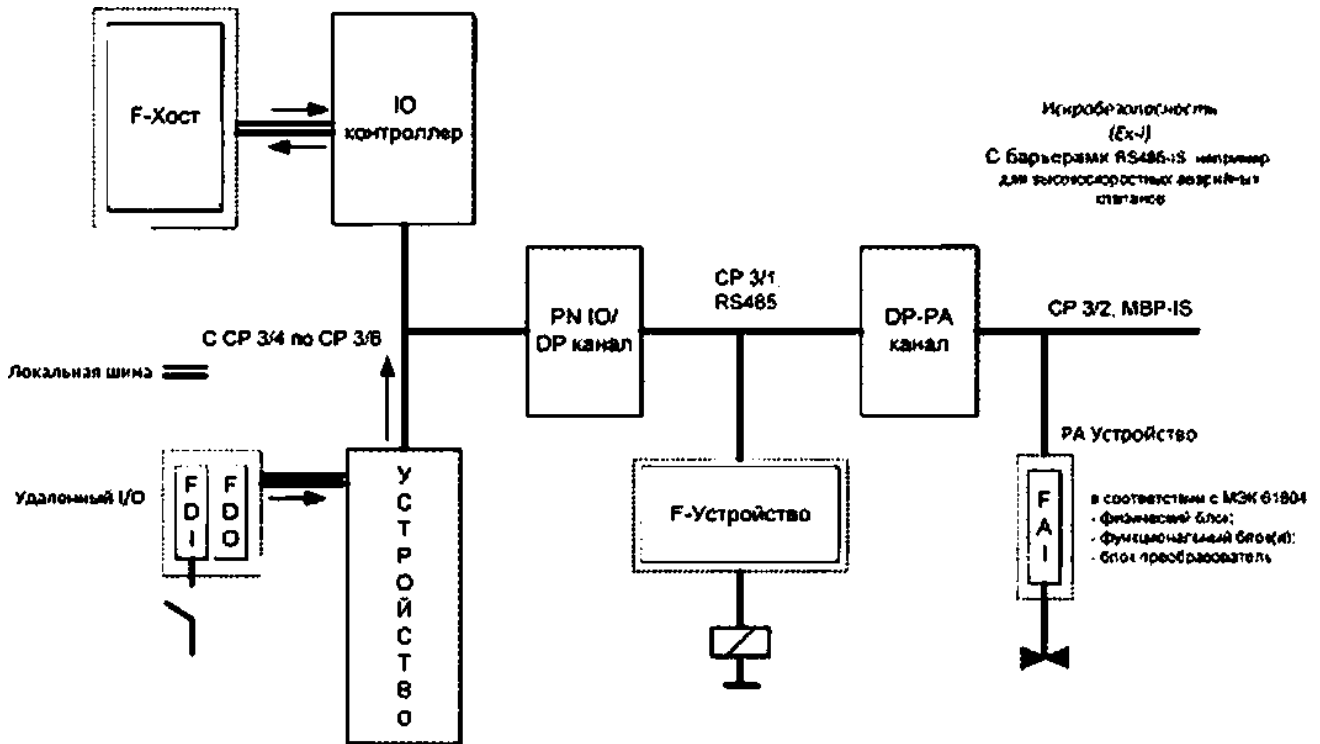


Рисунок 10 — Линейная структура шины

CP 3/RTE IRT) 2 IP- 9 10. (Real-Time. RT
IP- (11) IP- (3).
SCP 3/1: CP 3/RTE
LAN
F-
(7.3.9).



F- FSCP 3/1 F- CP 3/RTE (PN) F- (multi-controller / multi-master operation). « F- » F- V1 3/1 . [48].



- M6P-IS
- RS46S
- RS465-IS
- F-OI
- F-OO
- F-AI

RS46S

(61604).

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5.5 FAL (DLL, Phi)

5.5.1

CP 3/RTE ()

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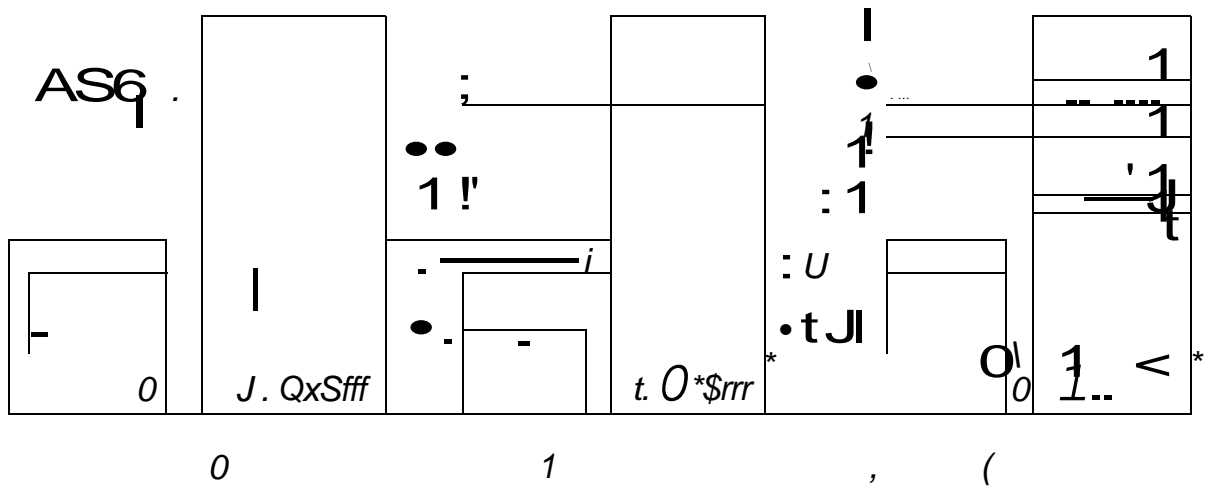
3/1,

I/O

3/1,

3/RTE

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3/4 no 3/6 (API * 0)

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(ASE)

5.5.2

GSD

CP 3/RTE

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ASE

«Connect» (« »),

CP 3/RTE

FSCP 3/1

IO-

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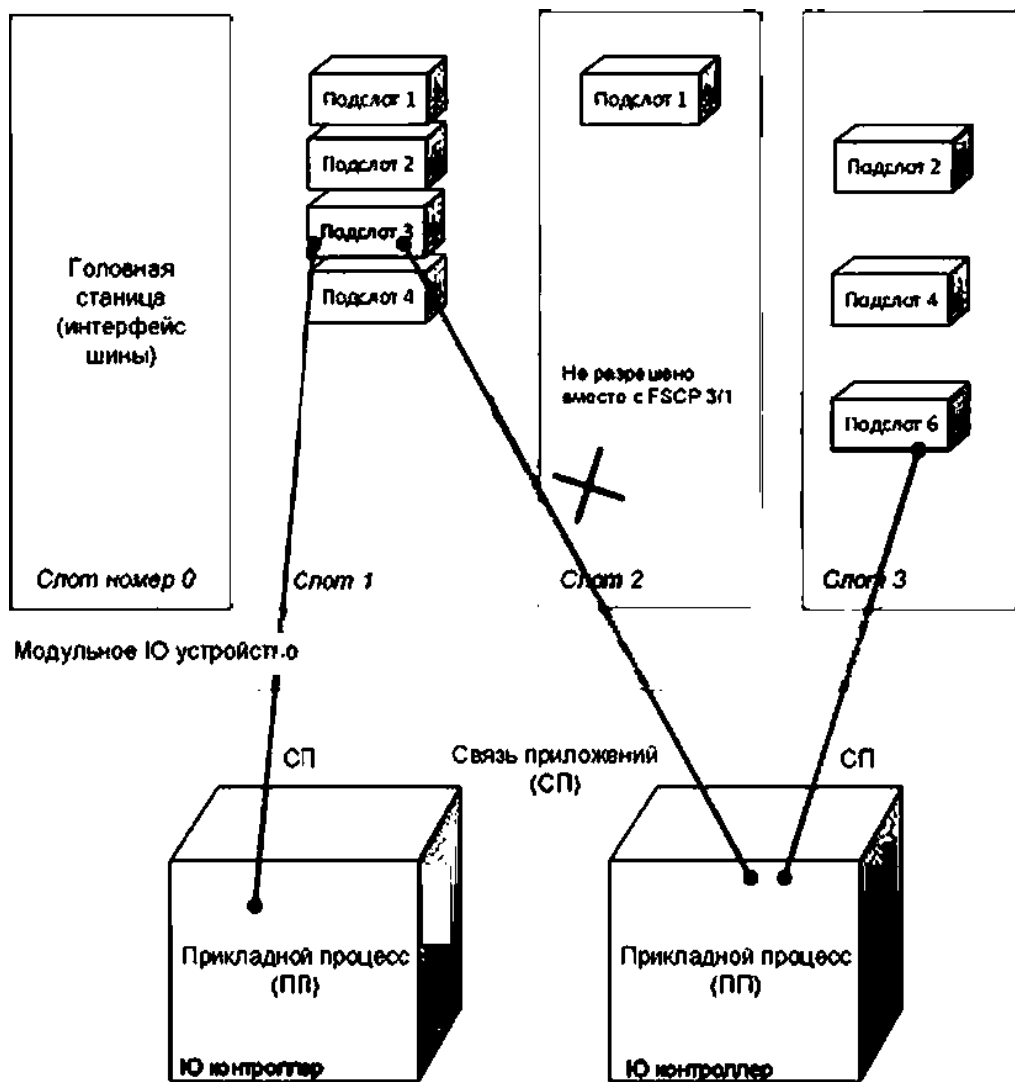
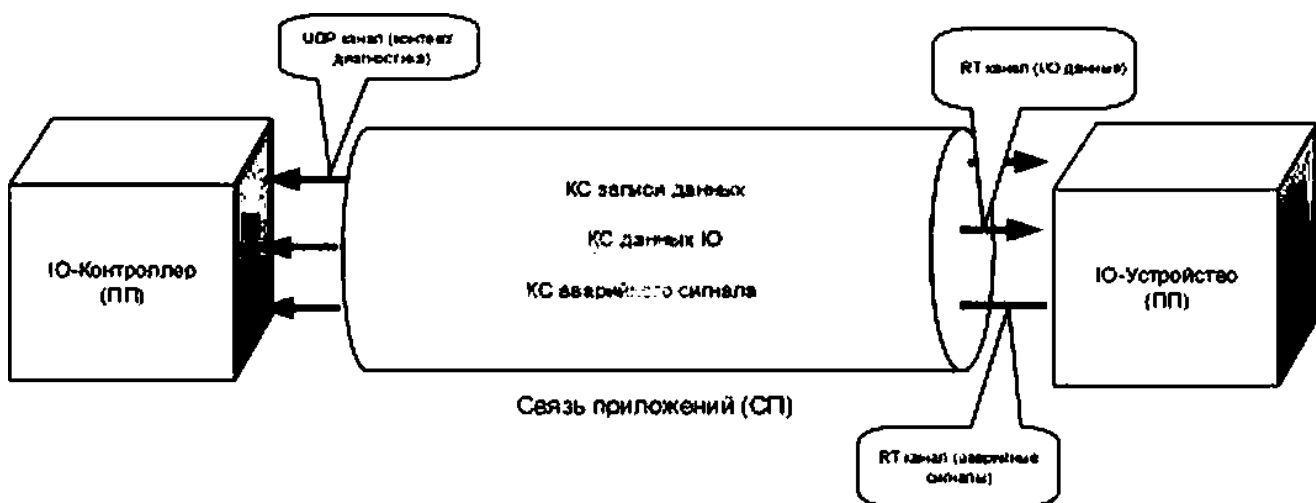


Рисунок 14 — Связи приложений модульного устройства



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IO - « » IO -
 (15). « » IO -
 « » IO
 « ».

5.5.3 CP 3/RTE (FCS), 32 , 16.
 . FSCP 3/1

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SFD —
OA — {6 octetts),
SA — ( octets):
VLAN —
Elher — lherneH.; 8892h CP 4 CP 3//6 (2
>D — ( CP 3/4 / );
KDPS — « :: / 4/( )::
IOCS — : //( //080)::
— (2 ):: , 31,,25ps.:
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FCS — 2- (104C110B7h)..
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5.5.4

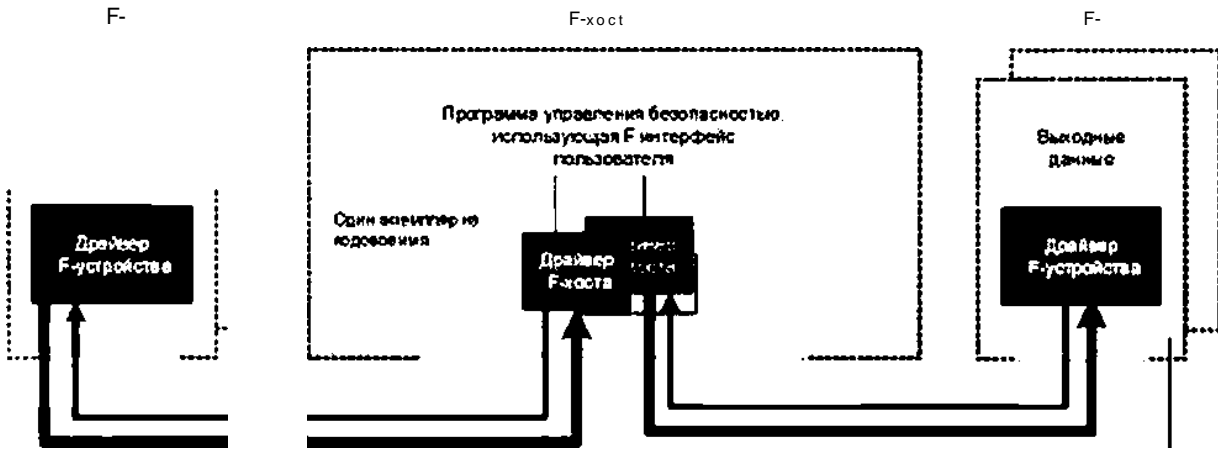
CPF 3

2.
 FSCP 3/1.

2— , FSCP 3/1

Integer8	6-	1	
Integer16	16-	2	VI V2
Integer32	32-	4	V2-
1 (64	64-	8	
Unsigned8 (used as Ms)	- ()	1	VI V2

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F-I/O
+

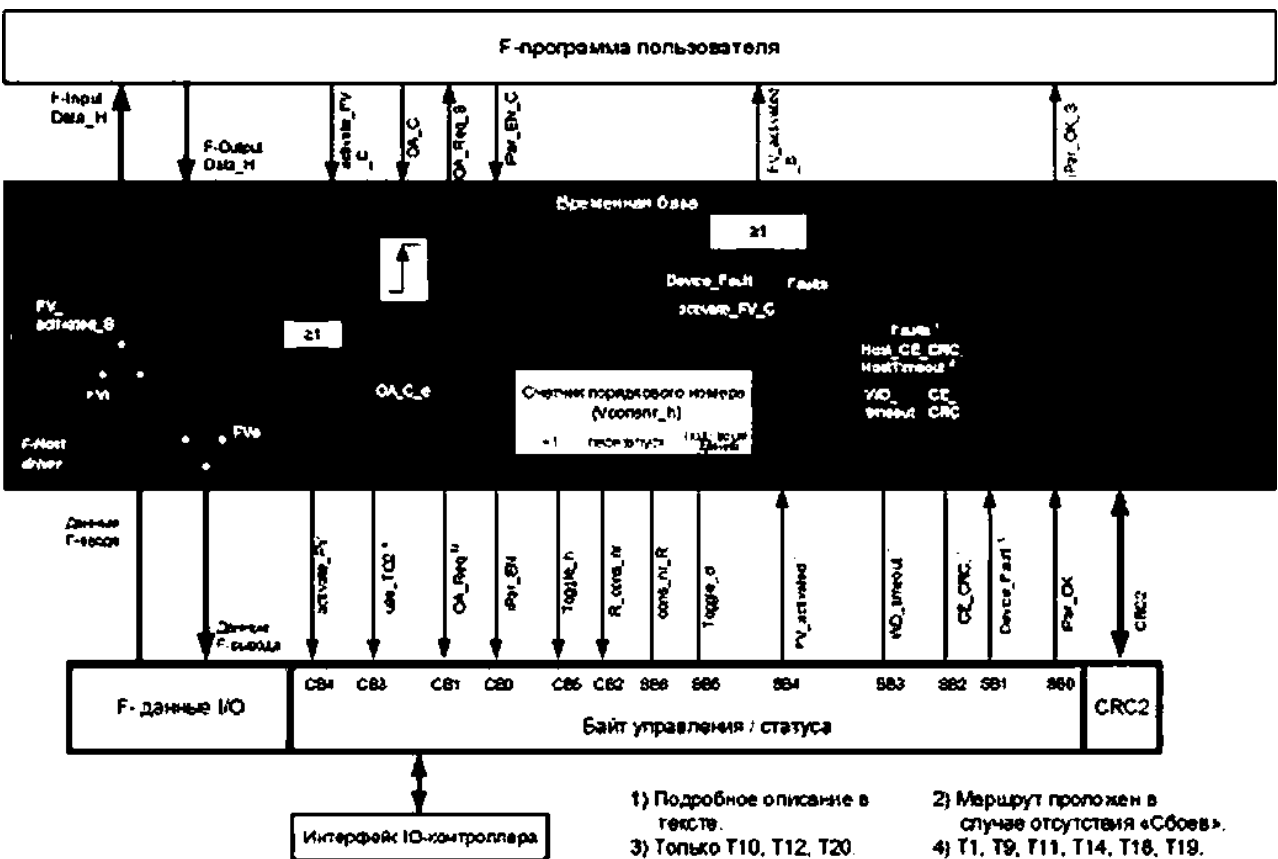
FSCP 3/1:

•
•
•

63 »

17 —

FSCP 3/1



18 — F-

F-

«_» (Control, . . .) «_S» (Status, . . .),

F- 8.5.2 62.

F- 8.5.3. 9.9

F- : activate FV

F- (:).

«1» (,) «0» F-

(,)

«1» 4 «1».

FVactivated S

F- (:).

«1» F-

(«0») F-

«1» «0» (

) *activate_FV» (4-).

iPar EN

(:), «1» F-

F- inape

EN» (0) «iPar_

«activate_FV_C» «1».

iPar OK S

(:) F-

F-I/O (41).

1 «iPar_OK» « 4, 8 17 F-

F- «iPar_EN_C» «activate_FV_C»

OA_C

F (:).

«1» (

) F-

OA_Req_S

(:) . 8 F-

F- F-

/

OA_Req_S (').

(' _ =

) F- OA_Req_S(" ").

PVi (*-F-Input_Data_D. . 19).

FVi , PVi F*Input_Data_D

(. 19).

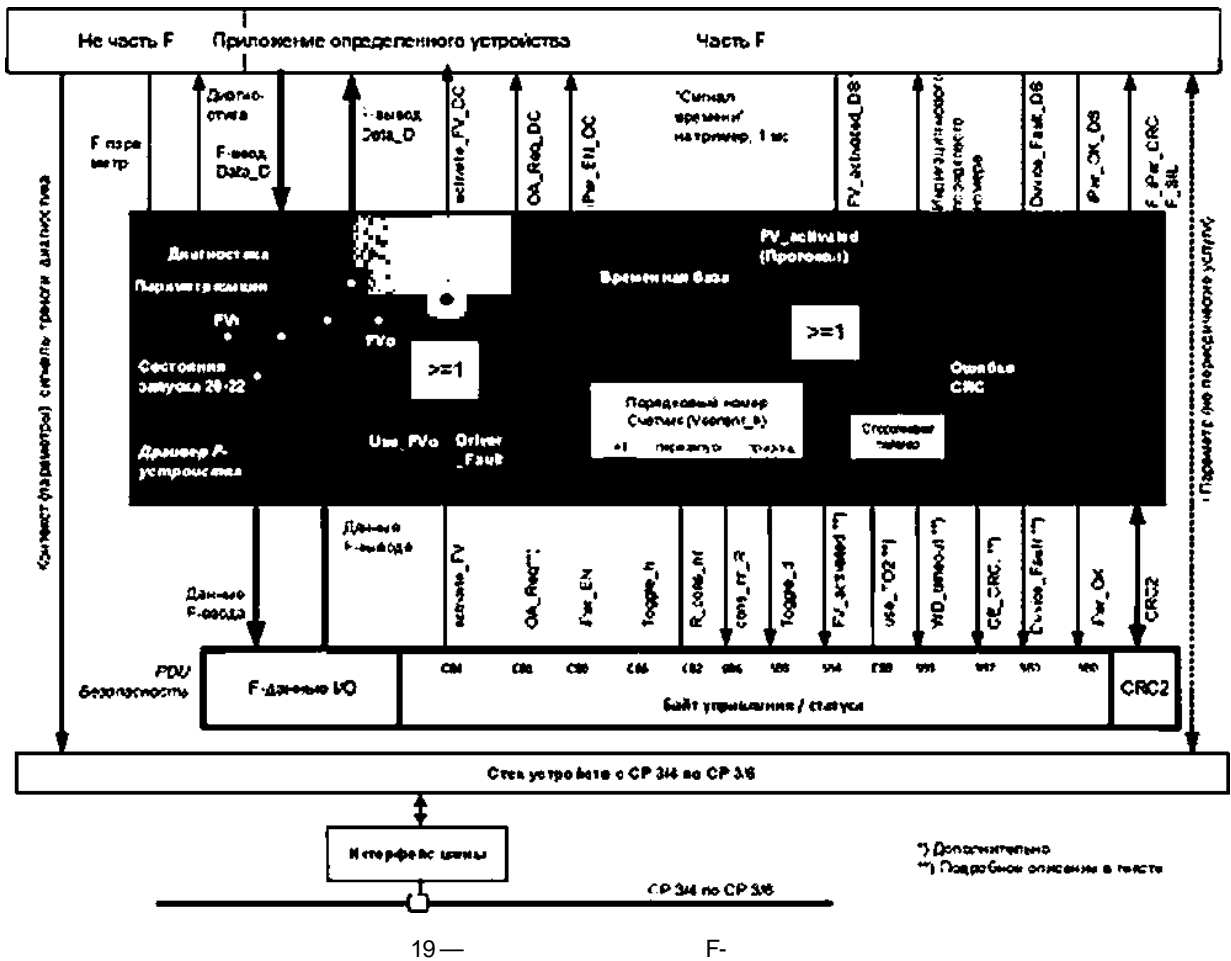
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PVo (—F-Output_Data_D).
 FVo (=0). PVo
 FOulput_Dat3_D.

6.2 F-
 19 F-
 CP 3/RTE

F- «F_iPar_CRC» «F.SIL»
 (1) F- PDU
 F- (

CP 3/RTE (5.5.2). F- I/O (FVi)
 (Control Bits) FVo). PDU
 1. 2. 4. 85.
 (FSCP 3'1 =) PDU
 (Status Bits) SBO ...S85.
 PDU Oriver_Fault



<0—*1; 1—0). R_cons_nr = «1» (+1), «Toggle_h»
 «Toggle_d» {0—1; 1—0). CRC («0»). PDU
 (CRC2).

«DC» (device control, . . . -
) «DS» (device status, . . .),

activate FV DC

F- (FV = 0).
 F-

FV activated DS

«1» . -
 («0») FSCP 3/1 . -
 : , . -
 «1» . -
 , «1» (:) , -
 («0») FSCP 3/1 -

OA_Req_DC

F- .
 , , (_ F-хос-
), , F- .

iPar EN DC

, , «1». -
 (F- -
 () .

iPar OK DS

, F- , «1». -
) , ((. -

Device_Fault_DS

6.3

6.3.1

F- I/O CRC2 . -
 . -

6.3.2

F- , iPar-Server F- FSCP 3/1

F- . CP 3/RTE . -
 CP 3/RTE . -
) - - - «ChannelErrorType» (-
 FSCP 3/1. 3 -
 FSCP 3/1 F- .

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3—

0x0040	64	(F_Dest_Add), .8.1.2
0x0041	65	(F_Dest_Add). .8.1.2
0x0042	66	(F_Source_Add). .8.1.2
0x0043	67	0 (F_WD_Time)
0x0044	68	«F_SIL» (SIL)
0x0045	69	F_CRC_Length
0x0046	70	F-
0x0047	71	CRC1
0x0048	72	,
0x0049	73	^
0 004	74	{
0x004	75	{ (iParCRC)
0 0 4	76	:
0x0040	77	:
0 4	78	:
0X004F	79	:

F- , iPar-Server -
F- , ' ,

F- ,

7

7.1 PDU

7.1.1 POU

20

POU

CP 3/RTE

PDU ,

I/O («

FSCP 3/1

() /

;

»),

I/O ()

F- 123

I/O

12

32-

CRC2(4)

24-

CRC2 (3)

F-

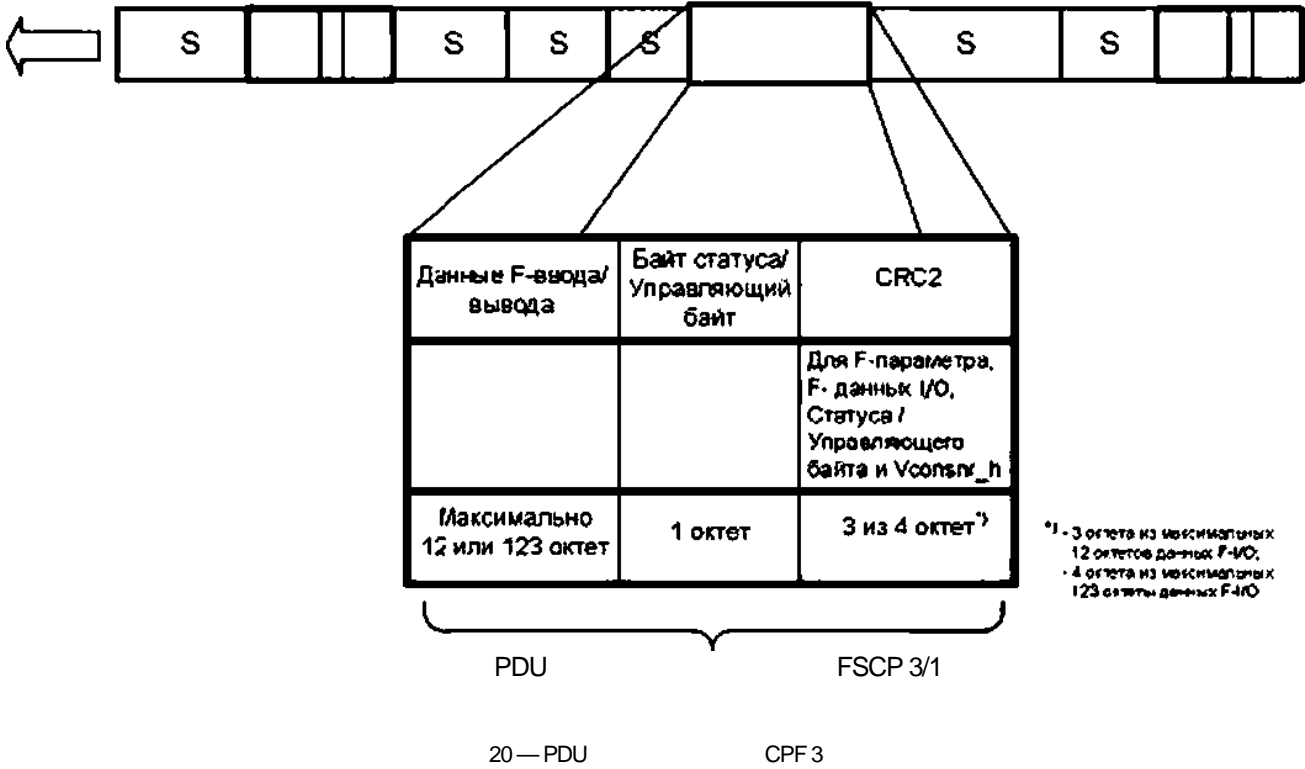
CRC2.

7.1.2

7.1.6

PDU

S - стандартные сообщения, включая PDU безопасности



7.1.2 I/O

F- I/O F- I/O CP 3/RTE PDU

61158-5-10. 8.5.2

24- 8 CRC F- I/O 12

CP 3/RTE (DAP), I/O, (13 14). F.

PDU CP 3/RTE PDU

4 5

4 5

7.1.3 (Status and Control Byte)

7	
res	VconsoleM)
•	cons_nr_R ToggleJ»

4 2

& :WD- F- F- *

<FV) CRC FV activated WD timeout CRC Device_Fault Par OK

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CP 3/RTE. 21. POU 20).

0) F- (-

1 «iPar_OK». (2) F- -

/) (-

2 — «Device_Fault». F- CRC2 F- 2-) . . . -

(CRC). F- -

9.5.1. — «CE_CRC». (-

3 F- F- — «WDjimeout». F- . . . -

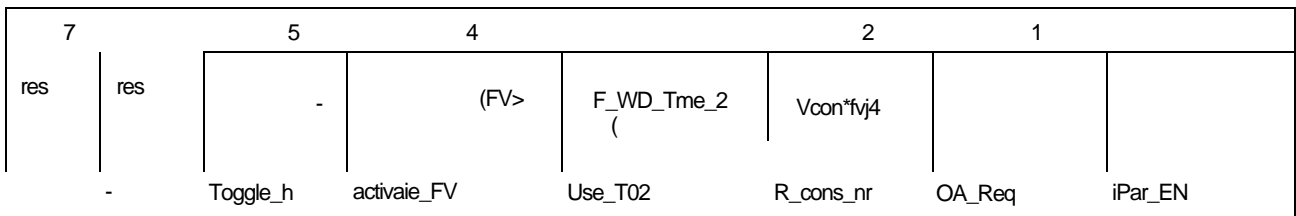
4 F- — «WDjimeout». FSCP 3/1 -

(19 7.2). F- -

5 — «Reactivated». (Toggle Bit), -

6 F- (Vconsnr_h). — «Toggle.d». Vconsnr_d.

7 — cons_nr_R. (res) FSCP 3/1.



22— (Control Byte)

22, PDU -

0 F- (20). F- -

(F- 1 i) — «iPar_EN». «OA_Reg_S*. -

2 «OA_Req». (_), , (9.1). •

(Vcopnsnr_d) F- «0» (. 7.1.4 7.2.5). 2 -

3 — «R_cons_nr». F- -

4 » « . F- « -

6.1. — «activate_FV». -

5 —

7.1.4. — «Toggle_h». (res) (Vconsnr.d). — *Toggle_h». FSCP 3/1. FSCP 3/1.

6 7 : «res» «0»

7.1.4 ()

«0»

V1. V2 24- 1... OFF FF FFh.

1 — V1 .8 (48).

(Vconsnr_h) * 1 2 3 4 5

Toggle_h (F-)

Toggle^d (F-)

(Vconsiu_d) F- 0 1 2 3 4

23—

V1, V2 PDU -

» PDU 24- ,

F- (Vconsnr_h) F- (Vconsnr.d), (23). -

CRC2. CRC2 PDU (24). ,

()

« » - (0—1.1—0). F- F- 24

F- F- R_cons_nr=* (.7.1.3). -

F- () , -

«Vconsnr_h». () . -

F-		CRC2
	1	F- I/O, Vconsnr_h
. 12 123	1	3 4

Toggle.h

(5)

R..consjir

(2)

Vconsnr d

(F-)
{ 0.1.0FFFFFFH }

Изменить Toggle_d
0→1 или 1→0 при
приращении

• Vcontv.d
• « CRC2

24—

F-yc

7.1.5

CRC2

F- (F-) F- , CRC1 8.3.3.2. CRC1. (I/O. = 0) CRC1 (CRC1). CRC2 (CRC2) F- (Vconsnr_h Vconsnr_d) CRC1 CRC2. CRC2. CRC2 (CRC2) F- (25). CRC1 F- CRC2 (CRC2) F- (Vconsnr_h Vconsnr_d) («0». 32-) («0». «1»)

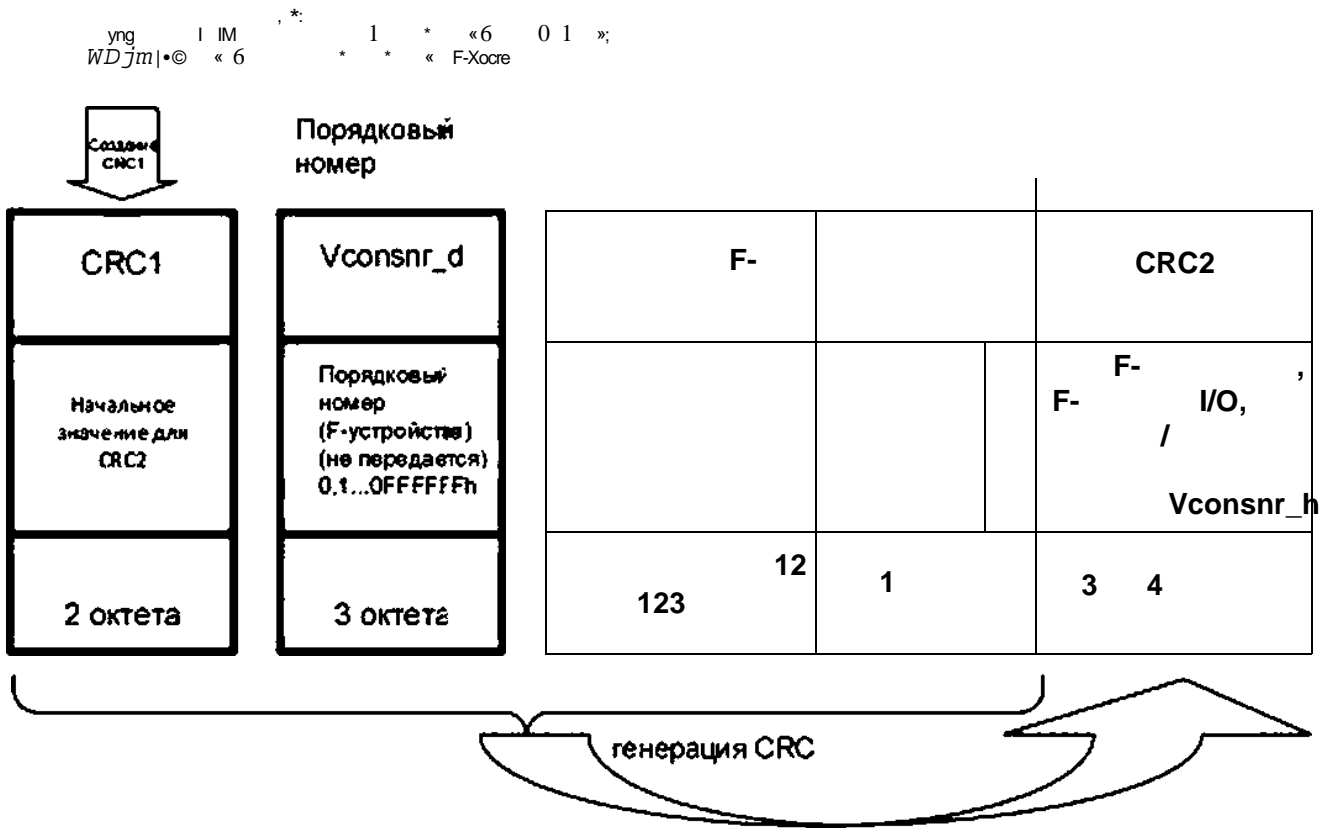
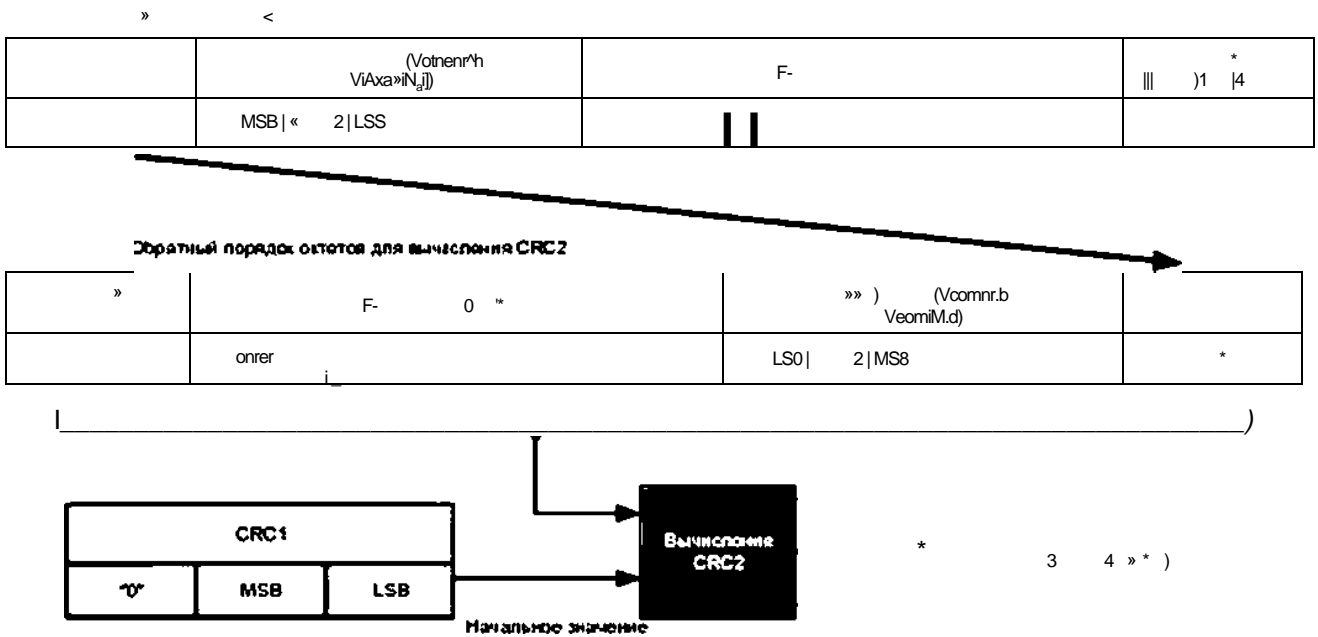


Рисунок 25 — Генерация CRC2 (вывод F-хоста)



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7.1.6 I/O PDU F- CP 3/RTE F-

7.2 FSCP 3/1

7.2.1 F- F- -

7.2.2 7.2.3. 27 -

7.2.5 PDU 7.2.6. -

F-

F-yc

[Redacted]

[Redacted]

PDU

CRC

CRC

» « 27 » (

7.2.2 F- F- 4 -

28 F- UML2. -

([-

4. 7 10 (Check Device Ack. UML2 « ») -

-

-

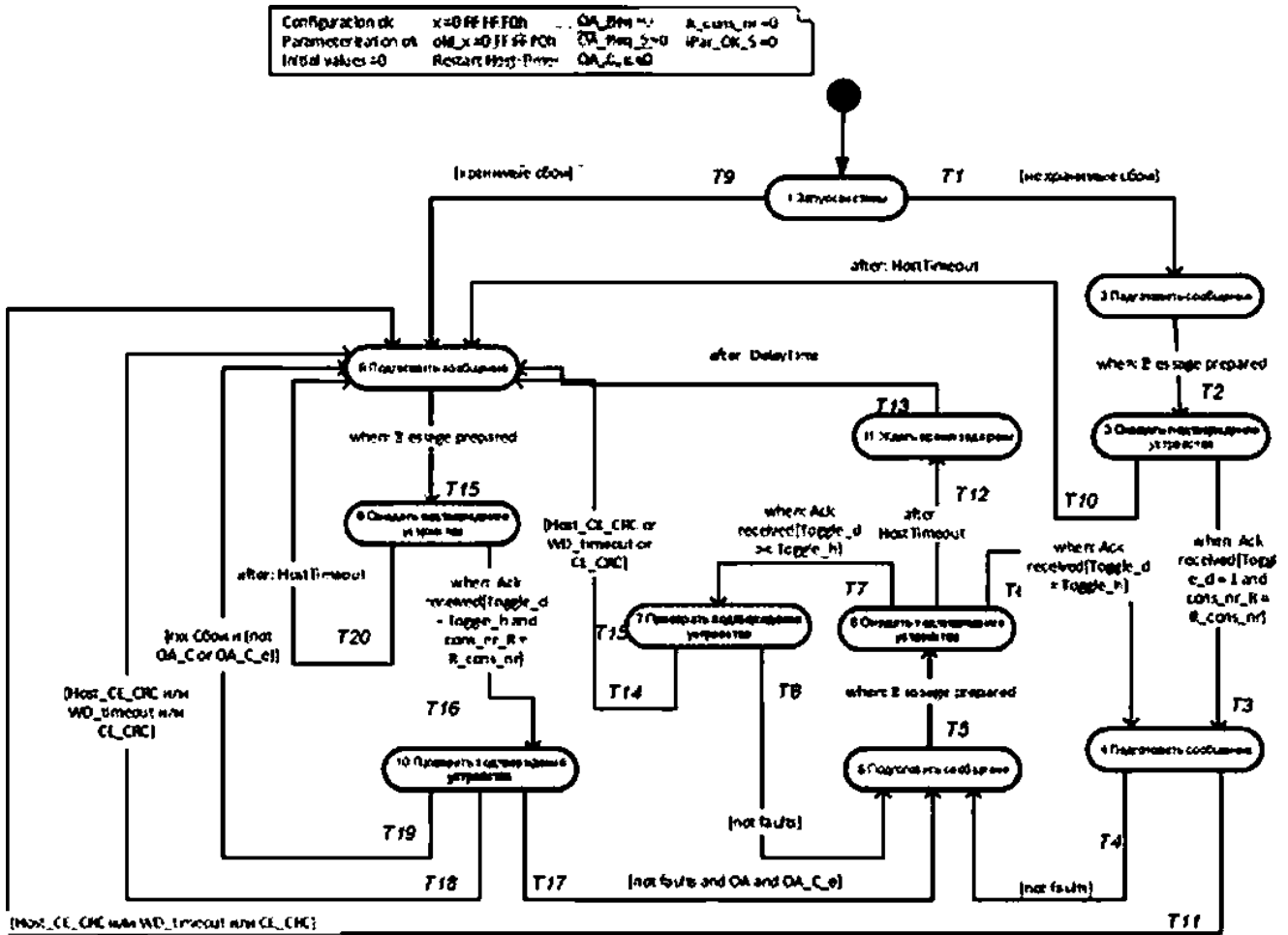


Рисунок 28 — Диаграмма состояний F-хоста

28. PDU « »;

HostTimeout — i-;

Host_CE_CRC — F- CRC. PDU ;

Device_Fault — F- : = 1;

CE_CRC — F- CRC F- : 2 = 1;

OA_C_e — (0—1):

WD_timeout — F- F- ; 3 = 1;

[] — UML () (1).

: Host_CE_CRC CE_CRC - WJmBOut; [] — (1).

: Host_CE_CRC - HostTimeout or - CE_CRC or - WJimeout.

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4—

F-

1 System Start { ()	F- T9. 1
2 Prepare message ()	PDU F-
3 Await Device Ack ()	PDU F- (-
4 Check Device Ack ()	POU SRC), CRC- (Host_CE_ (WD_timeout. CE_CRC)
5 Prepare message	PDU F-
6 Await Device Ack	PDU F- (-
7 Check Device Ack	PDU SRC), CRC- (Host_CE_ (old_x) (WDJimeout. CE_CRC)
8 Prepare message	PDU F- (-
9 Await Device Ack	PDU F-
10 Check Device Ack	PDU SRC), CRC- (Host_CE_ (WD_timeoirt. CE_CRC). (-)
11 wait delay time ()	(operator acknowledge) 0

4

1	1	2	FV. activate_FV «1. FV_activated_S=1 Toggle_h=1
2	2	3	PDU
	3	4	-
4	4	5	old_x = . = +1. if =01000000 then =1 Toggle_h = not Toggle_h. if FV_activated=1 or activate_FV_C *1 or Devnoa_Fault =1 then use FVi. FV_acbvated_s"=1 else use PVi. FV_activated_S *0 if acbvate_FV_C *1 or Devk»_Fault =1 then use FVo. activate_FV =1 else use PVo. activate FV =0 iPar_OK_S=iPar_OK
5	5	6	PDU

	6	4	-
7	6	7	-
8	7	5	<p>if Reactivated *1 activate_R/_C =1 or Dev*ce_Fault =1 then use FVi, FV_acbvtated_S =1 else use PVi. FV_activated_S =0 if activate_FV_C =1 or Device.Fault *1 then use FVo. activate_FV =1 else use PVo. activate_FV =0 iPar_OK_S =iPar_OK</p>
T9 ⁴¹	1	8	<p>use FV. activate_FV =1. FV_activated_S «1. Toggle_h *1. R cons nr »1, x =0</p>
10	3	8	<p>restart host-timer, store faults. use FV. activate_FV «1. FV_activated_S «1, Toggle_h = not Toggle_h. R cons nr=1. x =0</p>
11	4	8	<p>store faults. use FV. activate_FV =1. FV_activated_S «1, Toggle_h = not Toggle_h, R cons nr »1, x =0</p>
12	6	11	<p>use FV. activate_FV =1. FV_acbvtated_S «1, R cons nr=1. x =0</p>
13	11	8	<p>store faults. Togg(e_h = not Toggle_h, restart host-timer</p>
14	7	8	<p>restart host-timer, elnre fairfts. use FV. activate_FV =1. FV_activated_S «1, Toggle_h = not Toggle_h. R cons nr=1, x =0</p>
15	8	9	PDU
16	9	10	-
17	10	5	<p>reset stored faults. OA_Req_S *0. OA_Req =0. OA_C_e =0. R_cons_nr =0. okd_x =x. x»x+1. if x «01000000h thenx=1 Toggle_h = not Toggle_h. if FV_activated *1 or activate_FV_C =1 or Dev»ce_Fault =1 then use FVi. FV_actvtated_S =1 else use PVi. FV_activated_S =0 if activate_FV_C =1 or Device.Fault =1 then use FVo. activate_FV =1 else use PVo. activate_FV =0 iPar_OK_S =.Par_OK</p>

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4

18	10	8	store faults. OA_Req=0. OA_Req_S=0. OA_C_e=0. use FV. activate_FV = 1, FV_activated_S = 1. Toggle_h = not Toggle_h. R_cons_nr »1, x =0
19	10	8	OA Req_S=1. OA Req=1. if OA_C=0 then OA_C_e *1 use FV. activate_FV = 1. FV_activated_S = 1. Toggle_h = not Toggle_h. R_cons_nr=0. old_x=x. x=x+1. if x=01000000h then x=1
20	9		store faults. OA_Req=0. OA_Req_S=0. OA_C_e=0. use FV. activate_FV=1. FV_activated_S «1. Toggle_h = not Toggle_h. R_cons_nr=1. x=0, restart host-timer
#>	T9	*	.

4

X		F- . ^ Runinoun 8 F- . — 0... OFFFFFFh. C.RC.7 OFFFFFOh . F-xocr OFFFFFFh F- «0» «1»	
old_x			- -
DelayTime			/
host-timer			PDU F- — 0 ... 65 535
OA_C_e			() — 0—1 (). -

faults		(_), F- { F- , :
		F-) - Host_CE_CRC • HostTimeout - CE_CRC (2) - WD_timeout { 3)
()	-	« » {34}
()		« », , , « » {34}

7.2.3

F-

29

F-

5

UML2.

20

\$Mrtuo
Configurator
Pvameortaaon OK
irrsal Values «
IO4re -0

W) trnwouft
U»FVIf»0
ryjicavkMi-1
W>_t«wout_«wrc<c*_w_c*de\$ <0
4 CT.CHC.wunt 41
UW T02 Tbe »o

XsOffFTR»
Tng0».d*4

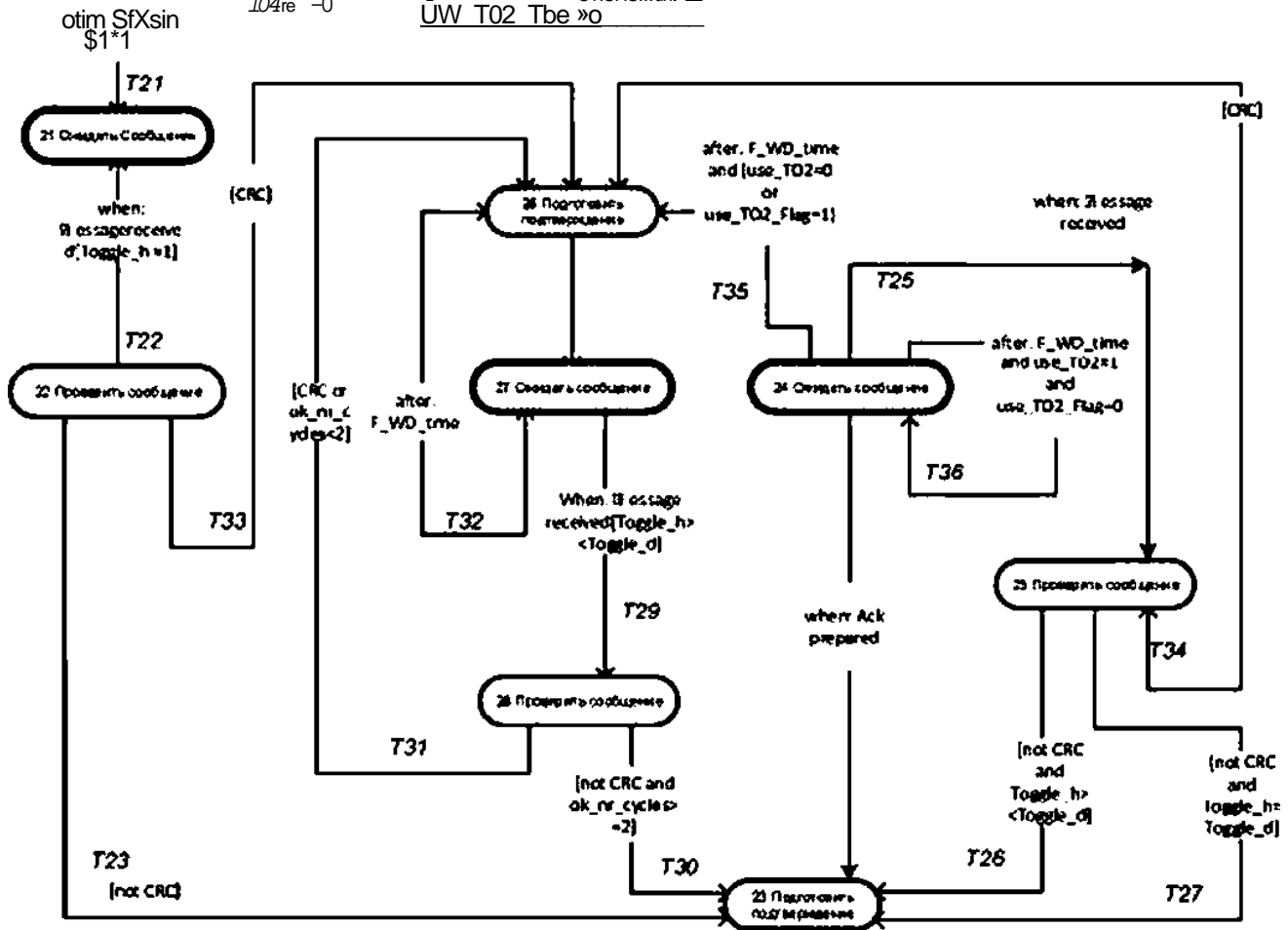


Рисунок 29 — Диаграмма состояний F-устройства

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29. :
 foggle_h = Toggle_d] — UML ()
 Toggle_h (»);
 (Toggle_h Togg»e_d] — Togg*e_h (« »);
 (CRC) — F- CRC (/):
 F_WD_Time — , F- «F_WD_Time»:
 use_T02 — ,
 F_WD_Time2;
 use_T02_Flag — ;
 Ack — POU F- ;
 Message received — PDU ; PDU ,
 0.

5 — F-

20 System Start ()			F- *0». F- «0».
21 Await message ()			PDU F-
22 Check Message ()			PDU CRC-
23 Prepare Ack ()			PDU F- ()
24 Await message			POU F-
25 Check Message			PDU CRC- ,
26 Prepare Ack			PDU F- ()
27 Await Message			POU F- ()
28 Check Message			PDU CRC- ,
T21	20	21	-
T22	21	22	if R_cons_nr=1 then =0. cons_nr_R=1
T23	22	23	use PVi. FVo. FV activated=1. CE_CRC =0. WD_timeout=0. Toggte_d = Toggte_h. restart device-timer. ok_nr_cydes =ok_nr_cyctes +1
T24	23	24	send safety POU

#5

25	24	25	<pre> Toggle_h >< Toggle_d then restart device-timer x=x+1. if x =01000000h then x=1. cons_nr_R *0 if R_cons_nr =1 then x=0. cons_nr_R =1 if use_T02 =0 then use_T02_Flag =0 </pre>
26	29	23	<pre> Use PVi. Toggle_d = Toggle_h, if ok_nr_cycles <4 ok_nr_cycle =ok_nr_cycle +1 if ofc_nr_cycles <4 then use FVo. FV_activated =1 else use PVo. FV_activated =0 if acbvate_FV =1 then use FVo else use PVo </pre>
27	25	23	<pre> Use PVi. Toggle_d = Toggle_h. if ok_nr_cycles <4 then use FVo, FV_activated =1 else use PVo. FV_activated =0 if activate_FV =1 then use FVo else use PVo </pre>
28	26	27	Send safety PDU
29	27	28	<pre> if R_cons_nr =1 then x=0, cons_nr_R =1 else x=x+1. if x =0100000!»! then x =1. cons_nr_R =0 </pre>
	28	23	<pre> use PVi. FVo. FV_activated =1. Toggle_d = Toggle_h. restart device-timer. ok_nr_cycles =ok_nr_cycles +1 </pre>
31	28	26	<pre> Toggle_d = Toggle_h. restart device-timer, if CRC then CE.CRC =1, CE_CRC_count =1. ok_nr_cycles =0. else ok_nr_cycles =ok_nr_cycles +1. if CE_CRC_count >0 then CE_CRC =1. CE_CRC count = CE_CRC count-1, else CE.CRC =0. if WD_timeout_count >0 </pre>

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5

31	28	23	then WD_timeout =1. WD_timeout_count = WD_timeout_count -1 else WD.bmeout =0
32	27	25	Use PVi. FVo. FV.activated =1. WD_teneout =1. WD_timeout_count =1, ok_nr_cycles =0. restart device timer. Toggle_d = Toggle_h
	22	25	Use PVi. FVo. FV activated =1. CE_CRC =1, CE_CRC_coont =1, WD_timeout *0. ok_nr_cycles =0. restart device-timer. Toggle_d = Toggle_h
34	25	25	Use PVi. FVo. FV activated =1. CE_CRC =1, CE_CRC_count =1. ok_nr_cycle =0. restart device-timer. Toggle_d = Toggle_h
35	24	25	Use PVi. FVo. FV_activated =1. WD_t»meout =1. WD_tomeoul_count =1. ok_nr_cycles =0. restart device timer. Toggle_d = Toggle_h
36	24	24	restart device timer with F_WD_Time_2 use_T02_Flag =1
41	T9		.
X			x F- . F- , . 0—1 1—0. 0 FF FF FFh. «0» «1 > . 8 , «R_cons_nr». . . (1). CRC2 0 ... 0 FF FF FFh. 0 FF FF F0h
ok_nr_cycles			FVo FV_activated=1 F- 3 1 3
CE_CRC_count			«CE_CRC» 1 2. 0-1

42

5

WD_timeout_count		«WCMimeoub* 2 0—1	1
devrce-timer		POU 0 ... 65 535	«F_WD_Time»

7.2.4

30 — 33

F-

F-

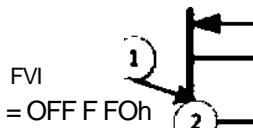
F-

F-

F-

F-

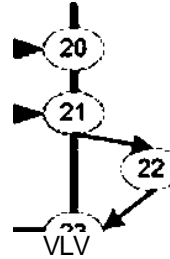
F-Xoct



= 0*)

Fvo, x = OFF FF F0
acbvate_FV = 1. R_cons_nr = 0

Pvi, x = OFF FF FOh
FV. activated = 1



FVo

FVo
x = OFF FF FOh

FVI
= +1

Fvo, x * OFF FF F1h
activate_FV = 0. R_cons_nr = 0

Pvi, x = OFF FF F1h
FV activated = 1

FVo
x = OFF FF F1h

FVI
* +1



Pvo, x * OFF FF F2h
activate_FV * 0. R_con\$nr» 0

Pvi, x * OFF FF F2h
fv_acbvated = 1

<24

25

FVo
x a OFF FF F2h

FVi
= +1

Pvo, x = OFF FF F3h
activate_FV » 0, R_cons_nr = 0

Pvi, x = OFF FF F3h
FV activated = 0

PVo
x » OFF FF F3h

PVi

* * }

30—

F- /F-

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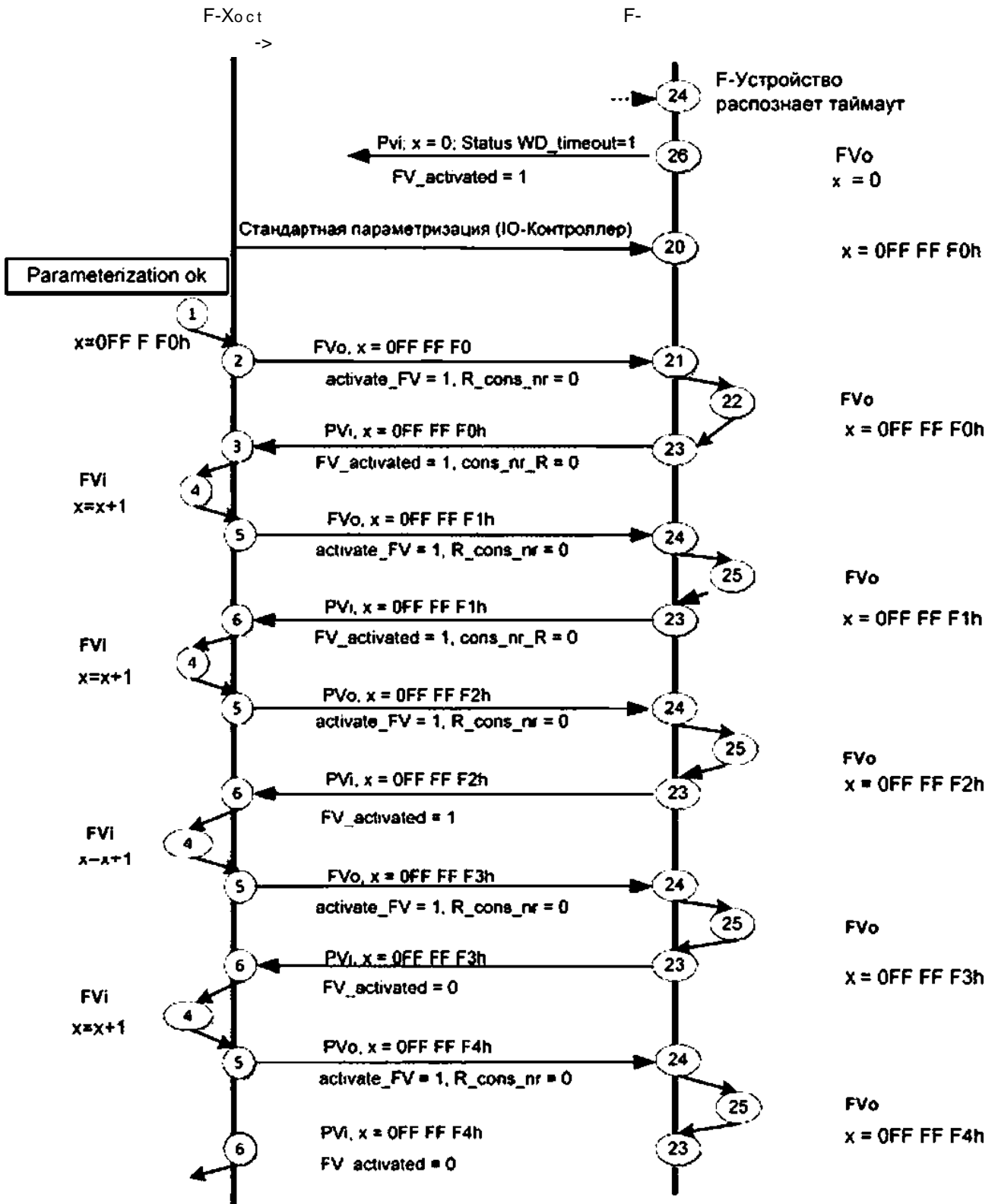


Рисунок 31 — Взаимодействие F-хоста / F-устройства во время отключения → включения питания F-хоста

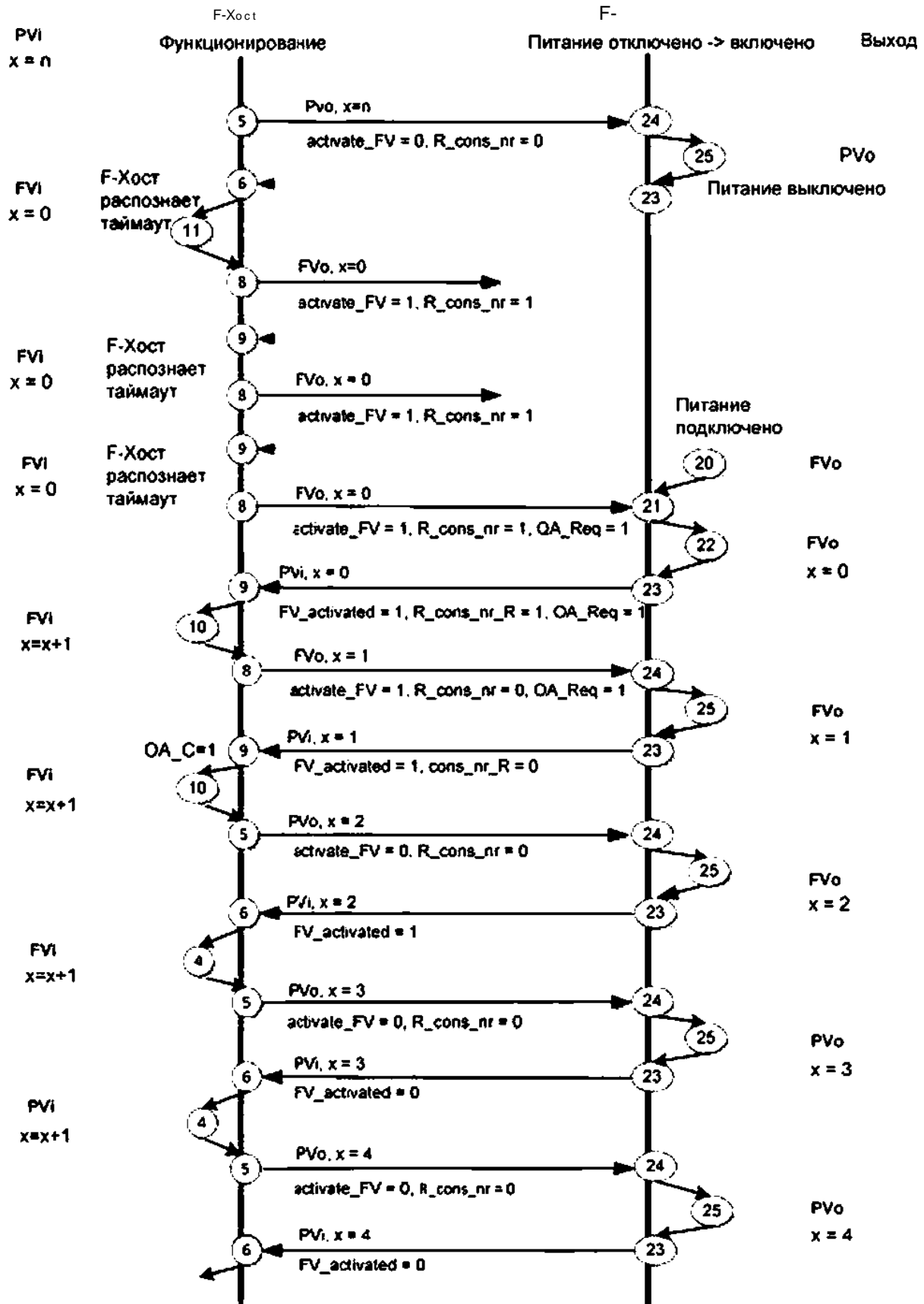
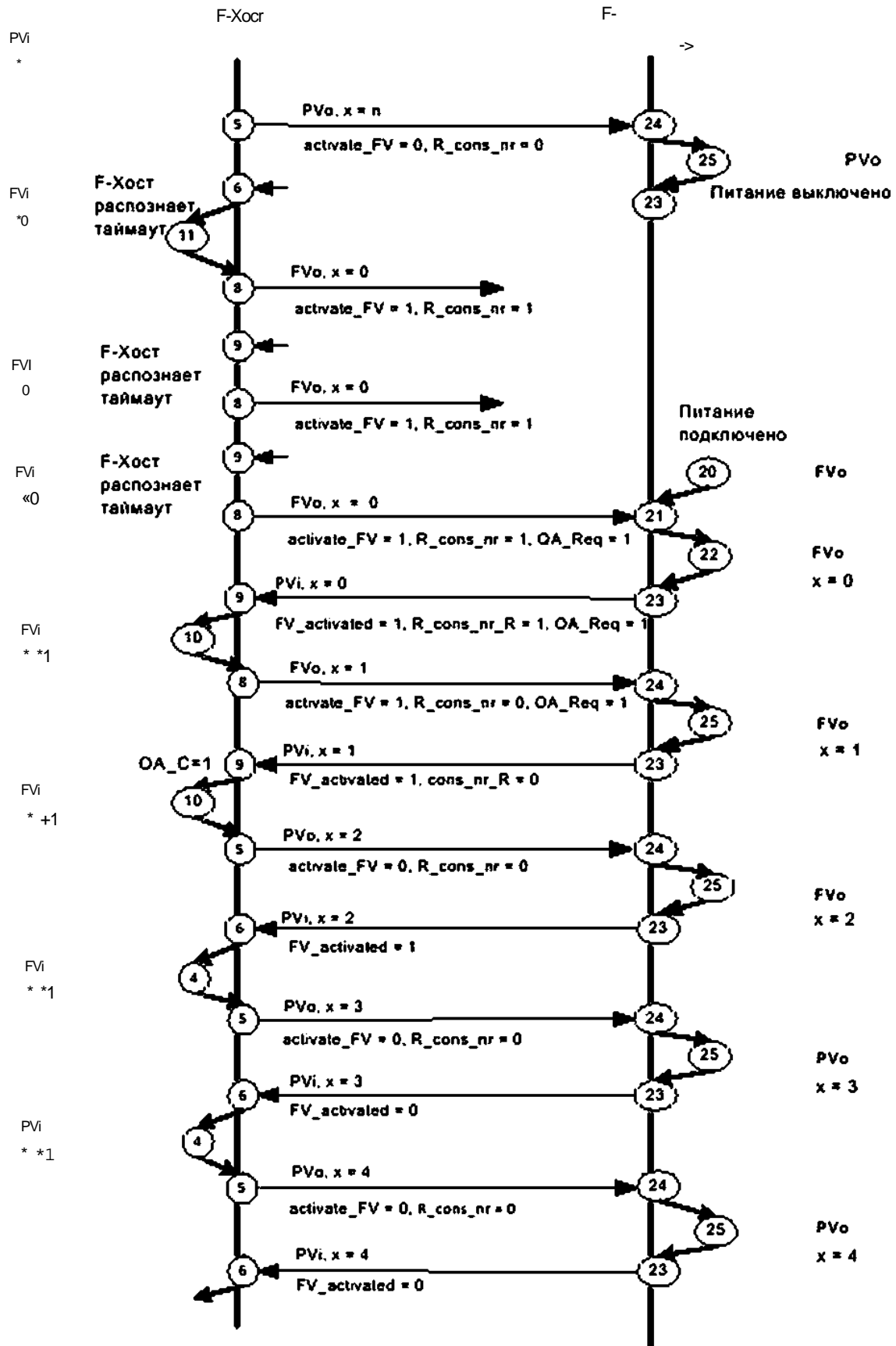


Рисунок 32 — Взаимодействие F-хоста / F-устройства с задержкой включения питания

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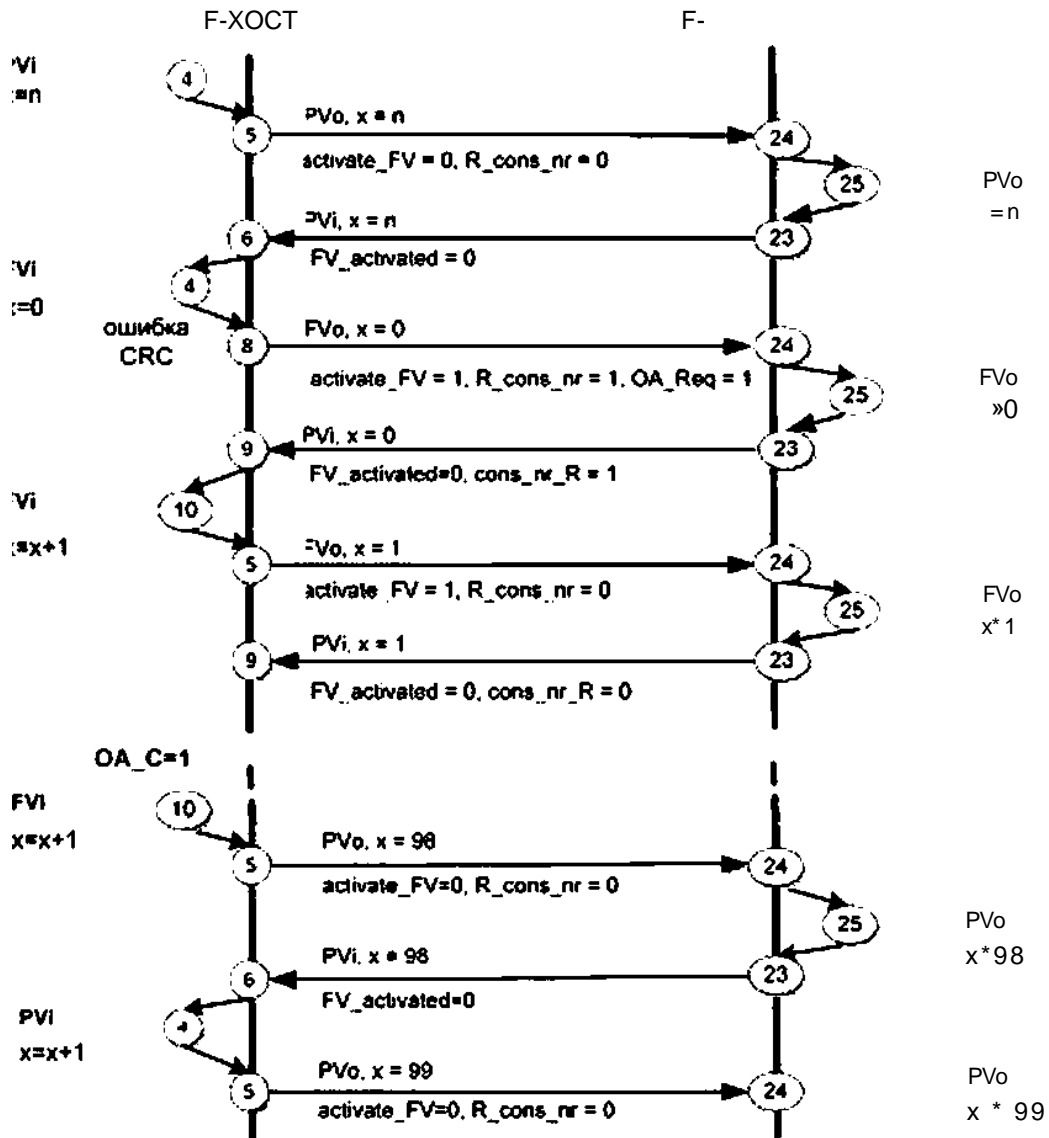


33—

F- /F-

34 35
CRC

F- F-



34—

F- /F- ,8

CRC

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F-X3CT

F-

=

PVI = -

FVt =0

FVI = +1

FVI = +1

FVI * *1

- @1

FVI = *1

PVI = +1

pvo.

activale_FV • 0, R_ontiv

PV). =

FV.adivaled =

FVo. = »1

activate.FV 1. R.cons.nr« 0

PVi Status CRC«1 x*n*1

FV_ect)V8tee*1

FVo. x @ 0

activate FV* 1. R_oon8.iv • t

PVt. status CE_CRC=1, x a 0

FV.actwaled=V cons.nr.R=t

FVo x=0

actwated.FV 1. R cons rv• 1

PVi. x=0

9 re-

FV.actvaieO=1. cons.nrR=i

PVo. x »i

1)-----

FV.acbvated *1 .R.cons nr 0

1 PVt. x 1

FV.ecbvateO = 1. con»_nr_R =

PVo. x »

| FV.aciwated • 1. R_cons_<v 0

1 PVi. X•2

0)-----

f FV.actwaied * 0

PVo. x•376

activate_FV*0. R.cons.nr » 0

PVi. x - 376

FV.acivaledcO

PVo. X » 377

activate FV*0, R cons nr • 0

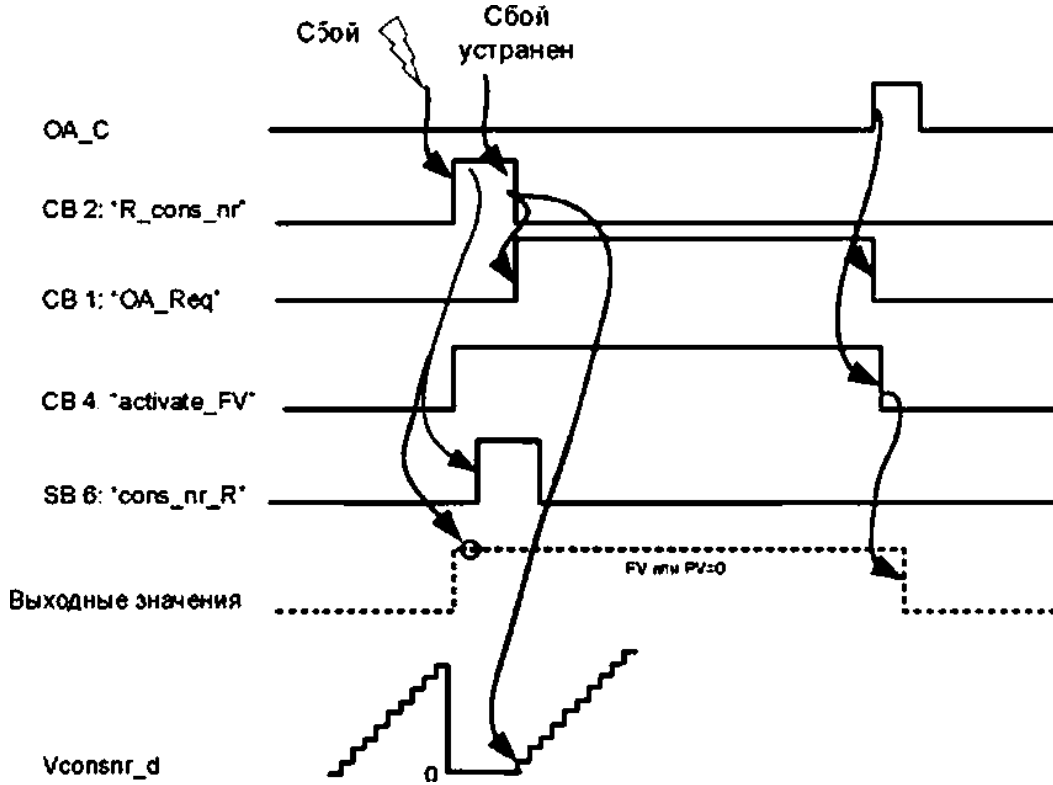
35—

F- iF-

CRC

7.2.5

2 36 F (=1)
 "R_cons_nr* F-
 «0». «0» (Vconsnr_d).
 (1) 4 «activatB_FV»
 (FV).



36—

F-хоср F- «OA_Req» 1
 (9.1) (_).

• (R_cons_nr = 0);
 • (_ = 1);
 • (OA_Req = 0);
 • (activate_FV = 0);

7.2.6

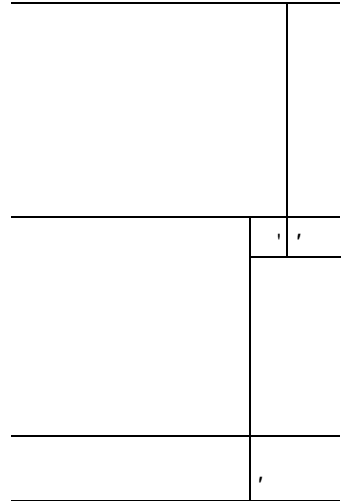
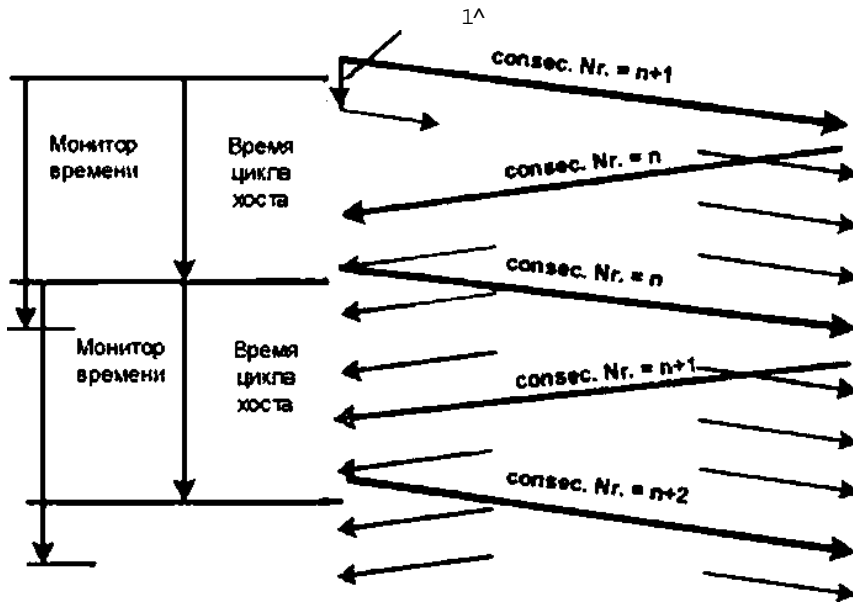
7.2.6.1

37 F CP 3/RTE
 IO- PDU F- CP 3/RTE
 POU = +1). 8 F- PDU F- (PDU PDU () -

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F-хост

F- ()



37—

F-хост — F-

37

F-

F-

38

PDU FSCP 3/1

F-

F-

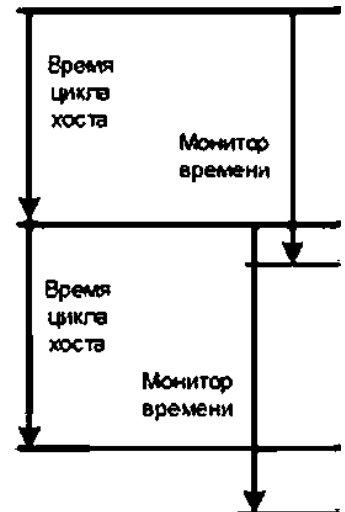
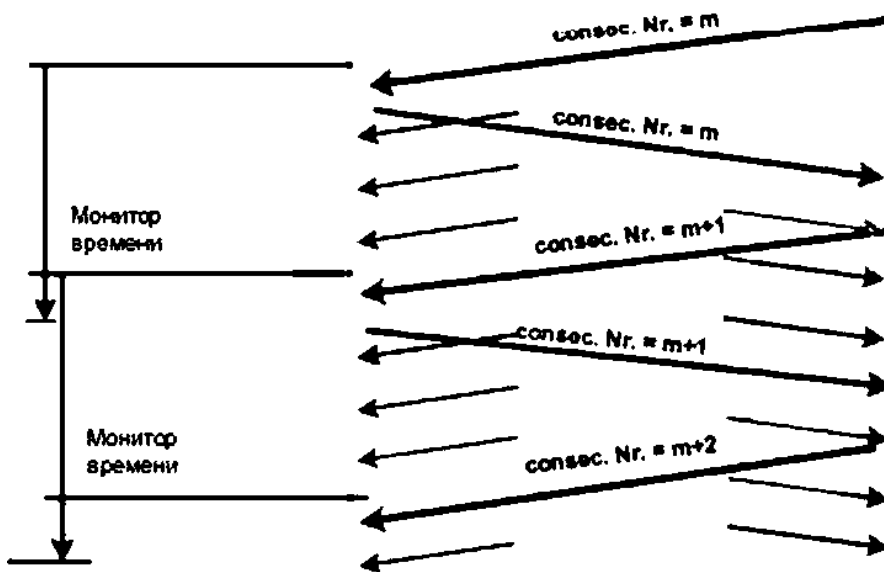
()

.

.

F- ()

F-ХОСТ



38—

F- — F-хост

: F-хосра
 («OFFFFFOh». F-хоср
 («OFFFFFFh» F-
 «3» «1»
 F-eeoAF-
 (FVo) 1. F-
 «0
 F- / - PDU F-)
 PDU { F-)
 F- F- ()
 F- PDU
 ()
 ()
 CP 3/RTE F-
 PDU
 PDU 1
 PDU 1 CRC2.
 PDU PDU
 (CRC)
 () € { } PDU = «0»
 ()
 28 ()
 ()
 F-
 F-

61784-3.3—2016

() (h) CRC (9.5.1). 8 6

6—

		POU	(h)
3	24	S 16	>10
2	24	\$ 16	>1
3	32	\$ 128	>10
3	32	S 128	>1

7.2.6.1

« , « , » [69]

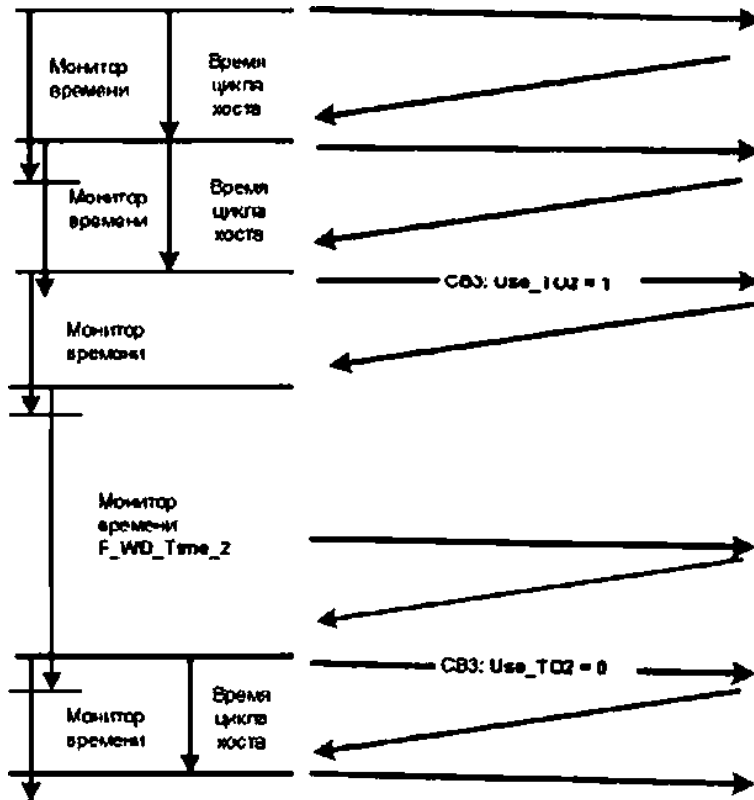
(F_WD_Tme),

F-
(F_WD_Tme_2)

39.

F-хост

F- ()



39—

7.3

7.3.1

: « (, - ,)» PDU - , .

7.3.2

« : « »).» (, -)

7.3.3

: « « »).» (, -)

7.3.4

: « : .» , , .

7.3.5

: CRC2 .» .

F-

PDU			
F- IO		()	CRC2
			F- WO. () F-
	1	3	3 4

F- WD : 6 .

40— F- CRC

CRC2 F- () F- I/O, - / (. 7.3.5 40). () F- /

7.3.6

: «1. . 2. , .»

61784-3.3—2016

:
 - ;
 - (;
 F-).
 9.3.3.
 7.3.7
 : « , -
 , .»
 : -
 (F-). F-
 F- (F-) CRC2.
 :
 , POU CRC2
 , F- F- CRC - () F- . 8
 F- (CRC CRC).
 F-
 • : (, F-):
 -
 • ; CPF 3.
 7.3.8
 : «1. -
 . 2. , .»
 , 9 10 -
 -
 -
 , -
 . 7
 7—

	()
	CRC (24)
	(2 16)
	(24)
	(24)
PDU , 3 .F-xocr	(24)
PDU .F-xocr	(24) (36)

• F- PDU / :

.F- PDU -

, F- - PDU =1(24).

-8 - 1 (24 -

).

7.3.9 : «1.

2. , .»

CP 3/RTE 11 PDU

F- 3. F-

F- « IP »

8

F-	F-	F-
F- 0, 1. 2- 8 - 11.	1) F- 0; 0 2) F- 1; 0 3) F- 0; 1 4) F- 1; 1	CP 3/RTE
F- 8 0. 1 (.PC.)	1) F- 0; 0 2)	- -

7.4

7.4.1

F- F- / CP 3/RTE. 3/RTE F-

F- (« »)

CP 3/RTE. F-

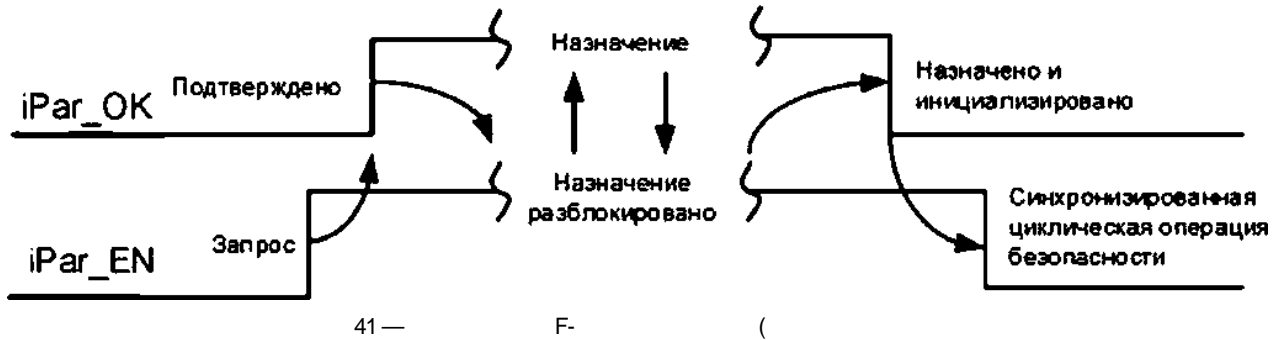
: — 1- (48).

15 CP 3/RTE. 61158-5-10.

61158-6-10 (55) F-

61784-3.3—2016

7.4.2 (F- (« 8.2})») «Write-Record» (PDU), (0 («F- (41). PDU (41). «iPar_EN_C» «iPar_OK_S». Proxy-FB-iParameterization. inap-ceperea (8.6.4> (8.6.2). 41



8

8.1 F-

8.1.1

CP 3/RTE. . . CP 3/RTE GSD GSD (. [43] [47]). F-

- F_S/D_Address « » :
- F_WD Time - / (GSD: F- /);
- F_WD Time 2 « » F- / - « »
- 17-2.6.2);
- F_Prm_Flag1 2
- F_Check_SeqNr V2: CRC2:
- F_Check_iPar
- F_SiL : = ?
- F_CRC_Length CRC2;
- F_Block_ID ;
- F_Par_Vers»on F-нарамерпоа/FSCP 3/1;
- F_iPar_CRC CRC (CPD ;
- F Par CRC CRC1 F-

8.1.2 F_Source/Destination_Address ()
 F- , F- F- .
 (2-) F- CP 3/RTE (5.4.2).
 («F_Source/Destination_Address» «F_S/D_»
 Adress»). F- F-
 F_S/D_Address
 CP 3/RTE (7.3.7). 0 OFFFh
 : «F_Source_Add» «F_Dest_Add»:
 Unsigned16.

8.1.3 F_WD_Time (F-)
 F- F- F-
 F- PDU
 F_WD_Time : Unsigned16. : 1
 : 1 65 535
 9.3.3.
 F- « » F_WD_Time
 GSD ().
 r_WO_Time 1:1.
 9.3.2.

8.1.4 F_WD_Time_2 (F-)
 F- F- F-
 F- / F- 42. F_WD_Time_2.
 : USe_T02

PDU

0

d

F-

F WD Time

F WD Time 2

42—

F_WD_Time_2

F_WD_Ttme_2
 : 1 65 535

: Unsigned16.

: 1 .

8.1.5 F_Prm_Flag1 ()

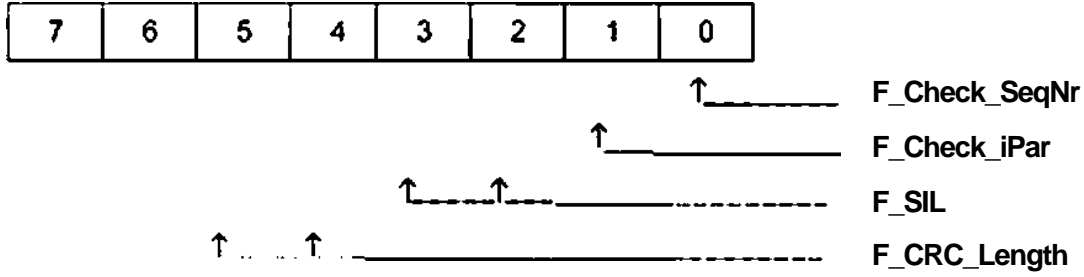
8.1.5.1 F_Prm_Flag1

8.1.5.2—8.1.5.5

F_Pmn_Flag1.

43.

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.7.1.3,

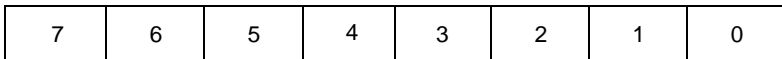
- .7.1.3,

43—F_Prm_Flag1

8.15.2 F_Check_SeqNr (CRC2)

CRC2 (. 44).

F- : 0 *F_Prm_Flag1».



V2. G\$0 (< jxwa-o « *»>. OSO (« 6 . « ») v2.

44—F_Check_SeqNr

8.1.5.3 F_Check_iPar

« ».

inap-cepeep. : 1 «F_Prm_Flag1«.



0 le GSO

1 « » { } . mohcsujoo « » « »

45—F_Cbeck_iPar

8.1.5.4 F_SIL () FSCP 3/1

(1 ... 3). F- (F_S1L). F- / , « » : 1 ... 3, (. 46). : 2 3 «F_Prm_Flag1».

7		5	4	3	2	1	0
---	--	---	---	---	---	---	---

0 0
0 1
1 0
1 1

1 (eGSO ' t>
6 2 (6S0 * 6 2J
63(yi*»«0' 6)
(<&> 1 >
• 6 »- | » . • Pt>Tq»4cnM

46—F_SIL

8.1.5.5 F_CRC_Length (CRC2)
F- - 1/0(12 123)
4 (. 47).
CRC2 POU
: 4 5

F- CRC 2,3
«F_Prm_Flag1».

7	6	5	4	3	2	1	0
---	---	---	---	---	---	---	---

0 0
0 1
1 0
1 1

CRC CSD < * W
'3-ByteCRCi
CRC 2 (VI
(2-Byie-CRC)
CRC «4 « (•
V1SV2.Q OSD < '4.
)
713. « »4

47—F_CRC_Length

8.1.6 F_Prm_Flag2 ()

8.1.6.1 F_Prm_Flag2
8.1.6.2—8.1.6.3

F_Prm_Flag2.

48.

7	6	5	4	3	2	1	0
---	---	---	---	---	---	---	---

_____ t _____

? _____

: .7.1.3.

F_BlockJO
F Par Version

48—F_Prm_Flag2

8.1.6.2 F_Block_ID ()

«F_BtackJD»
49). F_Block_tD

FSCP 3/1.

«F_Prm_Flag2»

61784-3.3—2016

7	6	5	4	3	2	1	0
---	---	---	---	---	---	---	---

0 0 0
 0 0 1
 0 1 0
 0 1 1
 1 0 0
 1 0 1
 1 1 0
 1 1 1

F_iPaf_CRC. F_WO_Time_2
 F Par CRC (51)
 F_WD_Time_2 (42) F_tPaf_CRC
 F_WO_Tm*_2 F_#>ar_CRC

49— F_Block_ID

8.1.6.3 F_Par_Version (F-)

(6.3.2 F- 50). , -

7	6	5	4	3	2	1	0
---	---	---	---	---	---	---	---

0 0
 0 1
 1 0
 1 1

VI (OSD
 V2 (GSO
 7.13,
 71.3.

50— F_Par_V@rsion

8.1.7 F_iPar_CRC (iPar.CRC { })

F- CRC (iPar_CRC) { CPD
 «0». «1».
 «FJPar_CRC». P-
 { F-
 » (8.6.4.S) F- F-
 F_iPar_CRC. (FV).
 : Unsigned32 3 F- «F_Prm_Flag2»

8.1.8 F_Par_CRC (CRC1 -)

CRC1 0. - CRC1 F- -
 CRC1 . 8.3.3.2. - 16- CRC (14EA8h). CRC1 -
 CRC2.

- 24 CRC (15D6DCSh) CRC2
- « ». xxx=CRC1.
- 32 CRC (1F4ACFB13h) CRC2
- «0000 », xxx=CRC1.
 : Unsigned16.

8.1.9 (record data object) -

F-

F_Prm-Block		I c CP I 3/1 CP 3/2. > [c CP I 3/4 CP 3/6	
F_Parameter	0	F_Prm_Rafl	Unsigned8
	1	F_Prm_Rag2	Unsigned8
	2	F Soiree Add	Unsigned16
	3		
	4	F_Dest_Add	Unsigned16
	5		
	6	F^VWD.Time	Unsigned16
	7		
		F_V0_Time_2	Unsigned16
	9		
	10	FjPar^CRC	
	11		
	12		Unsigned32
	13		
	14	F_Par_CRC	Unsigned16
End_F_Prm-Block	15		

51 — F-

51

F-

CP 3/RTE.

F_Parameter-Block (-

F- :
13).

8.1.10 F-

. 7.1.6.

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8.2 (IPar.CRC

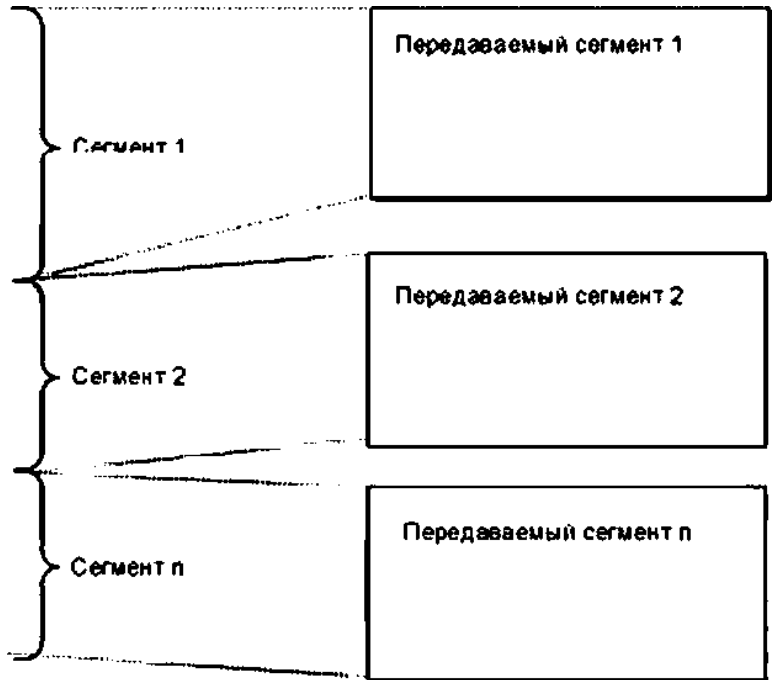
F-

F-

GSD (90)
 FSCP 3/1 52
 (222-1 ; 3/1 4
 240)
 52. CP 3/RTE
 CRC («Par_CRC#) 4- iPar_CRC (52)
 CRC
 «1». iPar_CRC (52.
 32- CRC FSCP3/1
 F_source/destination ()
 (52)
 CPF 3. /
 (52.
 ()
 52.

>Pat_CRC • CRC * 4
 ()
 F_Soince/Oest_Add ()
 1 » *)
 ()

*) /2 *
 240
 3/4 3/6



52— (

(, . 8.6.

8.3

8.3.1

FSCP 3/1 « » F- F (-
 F- () F-
 (,
 FSCP 3/1. (Proxy-FB) ((49)
 F- 8.6.2. I/O.
 «inap- ».
 F- / inap-cepeep
 (- 8.6.4. F- F-
 GSD () GSD
 F-
 F-napaMe~pa F- / -
 «FJO.StructureDeecCRC»
 F- I/O
 F-

8.3.2

GSDL GSDML

8.3.2.1

GSDL

FSCP 3/1 (GSDL) (43) (0 15745-3)
 F- F-
 51. F-
 (GSD), F. 9.
 F- 9— GSDL F- F-I/O

GSDL	
F_Ext_Module_Prm_Data_Len	F_Prm-BnoKa. 51. .14 18 F_iPar_CRC
F_Ex1_Module_Prm_Data_Const (offse)	F_Prm-EnoKa. 51. 4 0...3
F_Ext_Module_Prm_Data_Const (0)	F_Prm_Btock. F-iPar_CRC. .0x12
F_Ext_Module_Prm_Data_Const (1)	F_Prm- =5 (fix)
F_Ext_Module_Prm_Data_Const (2)	F-
F_Exl_Modute_Prm_Data_Const (3)	«0».
F_Exl_Module_Prm_Data_Rel (offset)	... 13 F_Prm-BnoKa.

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9

6SOL		
F_Ext_Module_Prm_Data_Ref (offset)	51.	4... 16. ExtUserPmData
F_ParamDescCRC	GSD	
F_IO_StructureDescCRC	F- GSD CRC0	.6.3.3.3
F_IO_StructureQescVersion	F- I/O (CRC7 . [43] 8.4.1
	CRC7. F- I/O.	1 16- 2 32- 1 CRC7.

GSDLcm. 8.6.4.6.

8.3 2.2
F-

GSDML

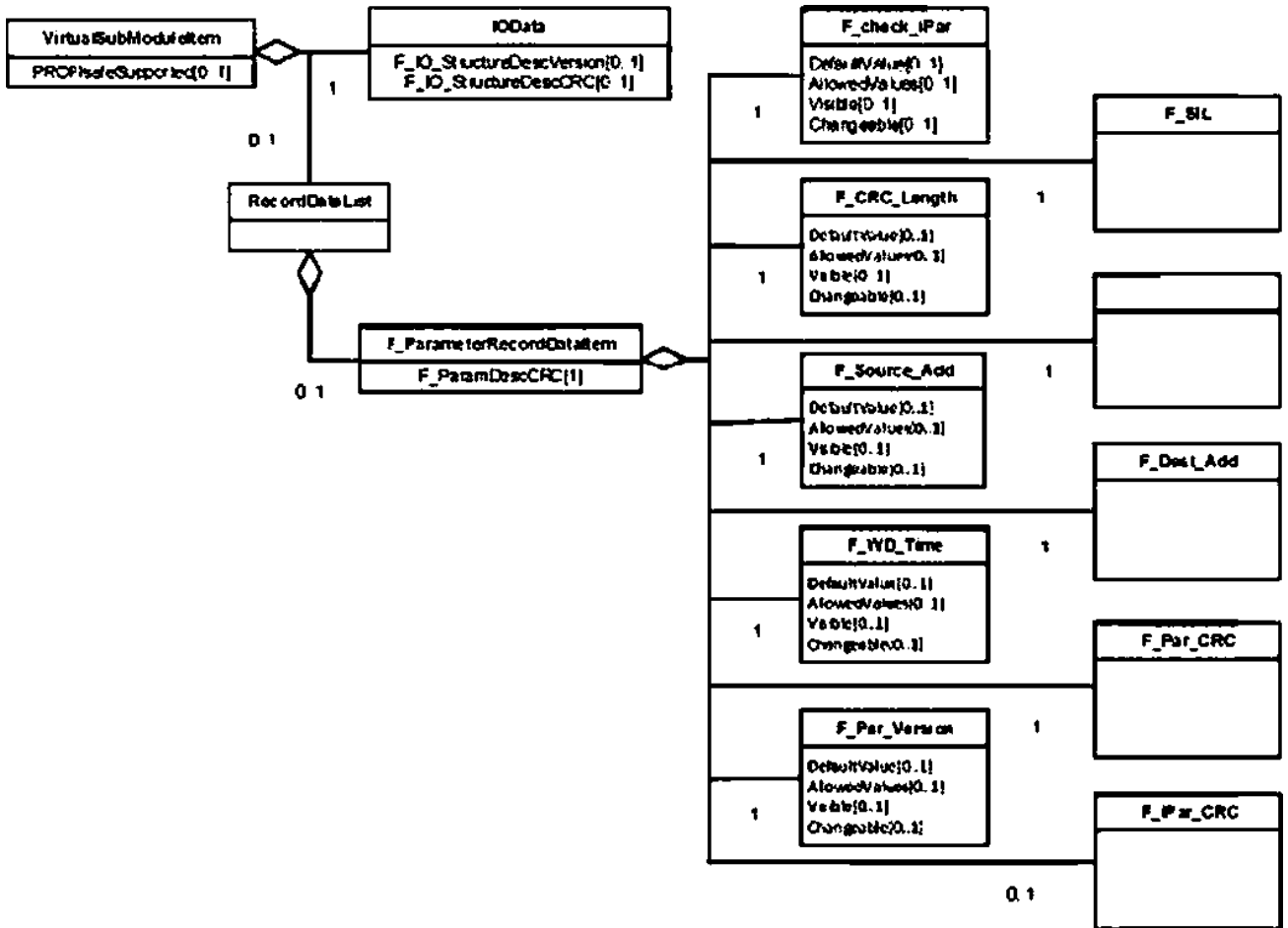
F-

GSO

(GSDML).

XML (

15745-3. 15745-4 [47]).

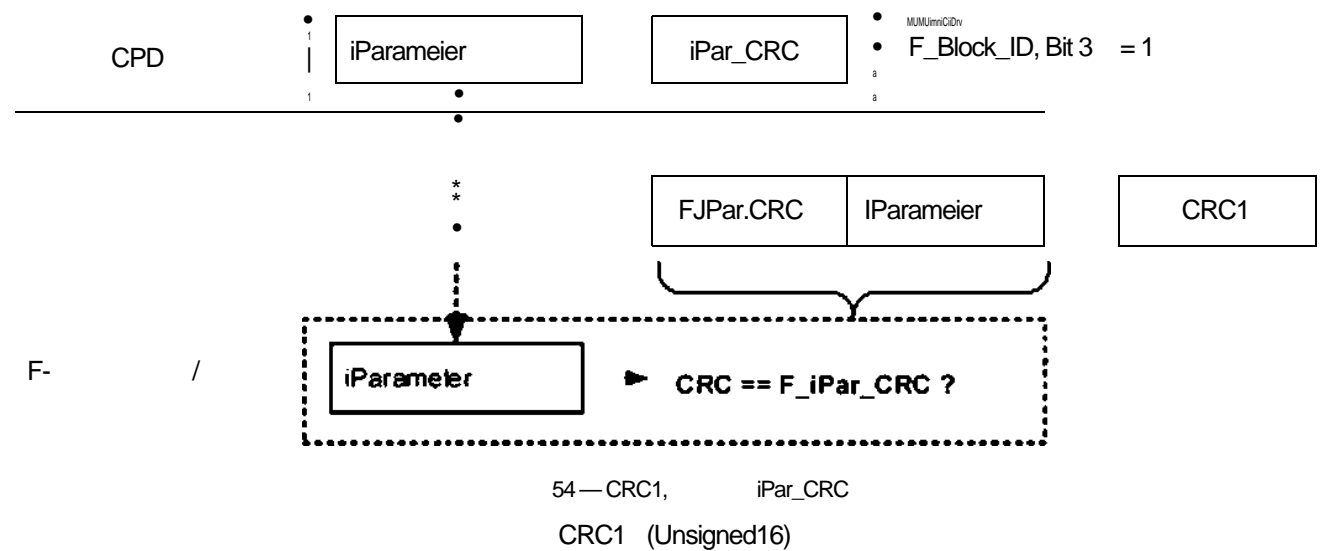


55 —

F-

GSDML

(CRC0) S3) GSDML. «VirtualSubmoduleItem» (-
) «F.ParamDescCRC». CRC
 F- S3. «F_IO_StructureDescCRC» «IOData»
 F- «F_10_StructureDescVersion»
 FJO (. 8.3.3).
 8.3.3 GSO
 8.3.3.1
 (F-). F- (), -
 I/O. F- CRC -
 F- CRC
 (GSD), -
 CRC 8.3.3.2 CRC1 iPar_CRC -
 25 CRC1 F-
 CRC2.
 CRC. 8.1.7. F- CRC 14EABh. CRC1 F- -
 3 F- «F_Block_ID» 51. CRC1 F_iPar_CRC. F_iPar_CRC
 «1»
 54.



6.3.3.3 CRC0 (. 7.1.5). GSD F- -
 CRC (CRC0). CRC. «F_ParamDescCRCn -
 16- CRC
 FSCP 3/1. (3/1 3/2) CRC0
 F_Ext_User_Prm_Data_Ref (4) «ExtUserPrmData» «PrmText». -
 F- 55 2- CRC0. -
 GSO , ,


```

while (F Parameter to read(0
<
    F_Paramete6eiPecof0OalaKtai
    e ( , ).
F-
t: F- (0: Bil / . 1. Uns>gned8. 2. Unsigned 16. 3 Urwified32>
BitORsel F- (0 Unsigned UnsignedieAJn»>gne632)
DefauKVaiue ( LoByte. . Unsigned8. Unsigned 16.
Unsigned32)
if (value range)#
<
;/ 1 AtonedVHue ( LoByte Lowefbmil)
' 2 « ABowedValue (* - LoByto Upperbmil)
)
dse t)
<
" > AilowedValue AilowedValueLis: AllowedValue
It (LoByte. HiByte)
)
//

```

£6— CRCO (GSDML)

GSD F- (CP 3/RTE) -

GSD:

F-

F-

(,)

F-

8.4

8.4.1

I/O (CRC7)

```

F- I/O «IOData» GSO. *F_IO_
StructureDescCRC» = CRC7. CRC7 10 ,
( 2). 32- CRC
(IF4ACFB13h). FSCP 3/1 5.5.4. 1
Unsigned8+Unsigned8. I/O VERSION (« ») Integer32
GSD VERSION,
Integer32 Unsigned8+Unsigned8. CRC7
CRC (14 ). CRC7
2 «FJO.StructureDescCRC» F-

```

10— I/O { 2)

VERSION	1	-
IN.ADDRESS.RANGE	2	I/O Input (F_MessageTrailer)
COUNT_PS_INPUT_BYTES_COMPOSITE	2	«Float32+Unsigned8» {5 }
COUNT_PS_INPUT_BYTES_U8_U8	2	*Unsigned8+Unsign>e(i8» (2)
COUNT_PS_INPUT_CHANNELS_BOOL_MAX	2	(« (, ») (1 1)
COUNT_PS_INPUT_BYTES_BOOL_M4X	2	(-) (, 1 1)
COUNT.PS.JNPUT.CHANNELSJNT	2	Integer 16

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10

COUNT_PS_INPUT_CHANNELS_DINT	2	. Integer32
COUNT_PS_JNPUT_CHANNELS_REAL	2	. Float32
OUT_ADDRESS_RANGE	2	. IOData Output (F_MessageTraler)
COUNT_PS_OUTPUT_BYTES_COMPOSITE	2	. «Float32+Uns«gned8» (5) -
COUNT_PS_OUTPUT_BYTES_U8_U8	2	. : « Unsigned8+Unsigned8» (2) -
COUNT_PS_OUTPUT_CHANNELS_BOOL	2	. (« »))
COUNT_PS_OUTPUT_BYTES_BODL	2	. : ()
COUNT_PS_OUTPUT_CHANNELS_INT	2	. Integer16
COUNT_PS_OUTPUT_CHANNELS_DINT	2	. tnteger32
COUNT_PS_OUTPUT_CHANNELS_REAL	2	. Float32
DATA_STRUCTURE_CRC	4	*F_IO_StructureOescCRC» = CRC7

8.4.2 Dataltem

8.4.2.1

8.4.2.2—8.4.25 Dataltem -
 F- 8.5.2, 10.
 FSCP 3/1 5.5.4. 32-
 Unsigned32.
 [67].

8.4.2.2 FJN_OUT_1

: 32-
 : 32-

Dataltem F_Channel_Driver FJNjDUT_1 57.

10

```

< >
< >:
<input C«mtStBA«y^AKiMaM (« >
< 14 < OataTyo»=*Wntitn*dSr UWA»IB£*I»im' Tor1d:*ip«U/>
<OaiM«m DaiaTrt»oz-f_ » * >>4 ' ^ / t>

<Ouput Corals Hncys'Atitaim ?
<Oaiaa«<n DaiaT>»pe=unt<t»«dsz' « > ;*««»• Taifd^Owipatt* f>
«OaiMem * . «» * '«1»14 * «» * »1« '>

< >: >

VERSION 02 02
IN ADRESS RANGE 08 08
COUNT PS INPUT BYTES COUPOSITE00 00
COUNT PS INPUT BYTES US U8 00 00
COUNT PS INPUT CHANNELS BOOL 32 2
COUNT PS INPUT BYTES BOOL 04 04
COUNT PS INPUT CHANNELS MT 00 00
COUNT PS INPUT CHANNELS ONTOO 00 00
COUNT PS INPUT CHANNELS REALOO 00 00
OUT ADDRESS RAN E 08 08
COUNT PS OUTPUT BYTES COMPOSITE 00 00
COUNT PS OUTPUT BYTES U8 USOO 00 00
COUNT PS OUTPUT CHANNELS BOOL 32 2
COUNT PS OUTPUT BYTES BOOL 04 04
COUNT PS OUTPUT CHANNELS INT » 00 00
COUNT PS OUTPUT CHANNELS DMT 00 00
COUNT PS OUTPUT CHANNELS REAL 09 00
DATA STRUCTURE CRC 0 9 8 « 2 0 9 32
    
```

57 — Dataltem F_IN_OUT_I

8.4.2.3 FJN_OUT_2

: 16-6 . 16-
: 1 . 16-

F_Channel_Driver FJN_OUT_2 58.

```

«      »
<Inpul Consiilcney:*Al>t*a» ccmnt* <K/>
      «Oaatam 1  >:* || 41* UAOA*BiB:* «* Texdd:*«iput» * »
<OsUKem 1 > >:* <|«> * UsaAtBi»:*MM* 1 * 1 1  >
«Oautem OauTypevj_M«»*saio*«r«ayt« Taxtids'bfety* f>
«*input»
«Output ConaHWncy:*Al>t«Bt comht«iKy*>
      «OiaUum OaiaTypaz'uufelMdi* UaaAsBittx'Pvd* Texdd**<Mpvb* f*
      «Oaatam  >:' «>4' « 4»:* * TtxBdx'AO chtrnN" 1»
      «Oaatam 1 * '( «>« *1 1 >«* * exflex* Wat/ i>
«^Output»
«< Oe«>

VERSION                                02
IN.ADORESS .RANGE                      08
COUNT PS INPUT BYTES COMPOSITE        00
COUNT PS INPUT BYTES US US            00
COUNT PS INPUT CHANNELS BOOL          IS
COUNT PS INPUT BYTES BOOL            02
COUNT PS INPUT CHANNELS INT           01
COUNT PS INPUT CHANNELS OMT           00
COUNT .PS JNPUT.CHANNELS.REAL         00
OUT ADORESS RANOE                      OS
COUNT PS OUTPUT BYTES COMPOSITE       00
COUNT PS OUTPUT BYTES US US          00
COUNT PS OUTPUT CHAAHELS BOOL         IS
COUNT PS OUTPUT BYTES BOOL           02
COUNT PS OUTPUT CHAWELS INT           01
COUNT PS OUTPUT CHAW1ELS OMT         00
COUNT .PS OUTPUT CHAM4ELS.REAL        00
OATASTRUCTURE CSC                      0xS22SS330
    
```

58 — Datallem F_IN_OUT_2

8.4.2.4 FJN_OUT_5

: (Float32+Unsigned8).

F_Channel_Drive F_IN_OUT_5 59.

```

<      > <1 4      eys'AlhMm* <oniittan(Y*>
      «Datallem 04(aTtM:*HoM32»«Im*iwdf' T6i*d:*Al cb4iw>*<' >
      «Datallem  >:' _ «*» «1 «* 14# 1** T<udd:'S't*ty* »
      « tu>
      <OUpulCenMBncya*AlliMm<oi>ht«iuv*>
      «Datallem OataTypaa*r_M«»wa«i*fe'Mri**      >

«/Output»

«>100 >

VERSION                                01
IN ADORESS RANGE                      09
COUNT PS INPUT BYTES COMPOSITE        09
count 'ps'input ~channel's BOOL        00
count .ps jinput ~8yt e8_bo 6i         00
COUNT PS INPUT CHANNELS INT           00
count 'ps'input 'ckannel's't eal       00
OUT AO ORE SS RANGE                    04
COUNT PS OUTPUT BYTES COMPOSITE       00
count 'ps'output 'chawel's BOOL        00
count 'ps'output bytes bool            00
count 'ps'output 'ckakmeis_int         00
COUNT PS OUTPUT CHAM4ELS REAL         00
DATA STRUCTURE .CRC                    0xSCAC
    
```

59 — Datallem F_IN_OUT_5

61784-3.3—2016

8.4.2.5 FJN_OUT_6

: (Float32 * Unsigned8). Unsigned8. Unsigned8,
 Unsigned8.

60. 10 Dataltem F_Channel_Driver F_IN_OUT_6 -

```

«      »
«Input Consistency^AB items consistency*»
«DataItem DataType**Float32*Unsigned8* TextId=* Al channel' f>
«DataItem DataType=*Unsigned8' UseAs8itS="f>be~ TextId=*St>tujr f>
«DataItem DataType=*Unsigned8' UseAs8itS="f>be' TextId=*St>tus2' />
«DataItem DataType=*Unsigned8' UseAs8itS="f>be' TextId=*St>tus3' f>
«DataItem DataType=*F_MesssgeTrailer4Byte* TextId=*Safety' />
«/Input»
«Output Consistency^All items consistencyV»
«DataItem DataType=*Float32+Unsigned8* UseAs8itS="false" TextId=*A0 channel' .»
«DataItem DataType=*F_MesssgeTrailer4Byte* TextId=*Safety' />
«/Output»
«IOData»
    
```

VERSION	01
IN ADDRESS RANGE	12
C6UWT_PS_INPUT_BYTES_COMPOSITE	05
COUNT PS INPUT CHANNELS 800L	24
COUNT PS INPUT BYTES BOOL	03
COUNT PS INPUT CHANNELS INT	00
COUNT PS INPUT CHANNELS REAL	00
OUT ADDRESS RANGE	09
COUNT PS OUTPUT BYTES COMPOSITE	05
COUNT PS OUTPUT CHANNELS_BOOL	00
COUNT PS OUTPUT BYTES BOOL	00
COUNT PS OUTPUT CHANNELS INT	00
COUNT PS OUTPUT CHANNELS_REAL	00
DATA_STRUCTURE_CRC	0xF33

60 — Dataltem F_IN_OUT_6

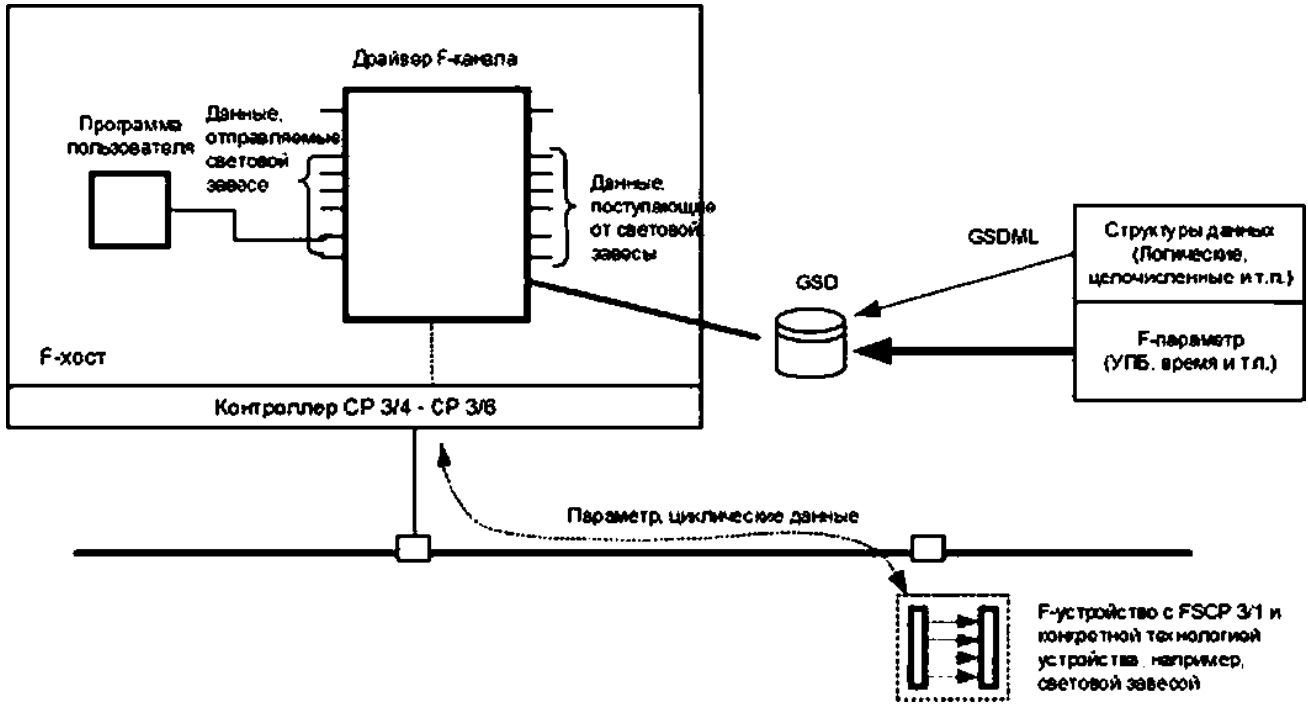
8.5

8.5.1 F-

F- I/O. F- F- (-
). (« F- ») / () -
 , / (,
). 61
 « F- ».

8.5.2

F- F- ,
 F- IODataSection GSD. : 8.3.2;
 - :
 - Float32 + Unsigned8. Unsigned8,
 Unsigned16, Unsigned32. Integer16.



61— F- « * F-

11

F- F- FSCP 3/1

F-

5.5.4. Unsigned32. 8-

PDU

32-

Unsigned8.

8.4.2.

11—

F-

F **i-eno**	F- ()	F- ()	
F_IN_OUT_1	32	32	,
F_IN_OUT_2	16 . 1Integer16	16 . 1Integer16	,
F_IN_OUT_5	1 Floa(32. Unsigned8 (- «)		,
F_IN_OUT_6	« 8- 24 « »: 1 Float32. »:	« »: 1 Float32, 8	,
^w He	GS3		,

-
-

«0»:

(, F-).

8.5.3

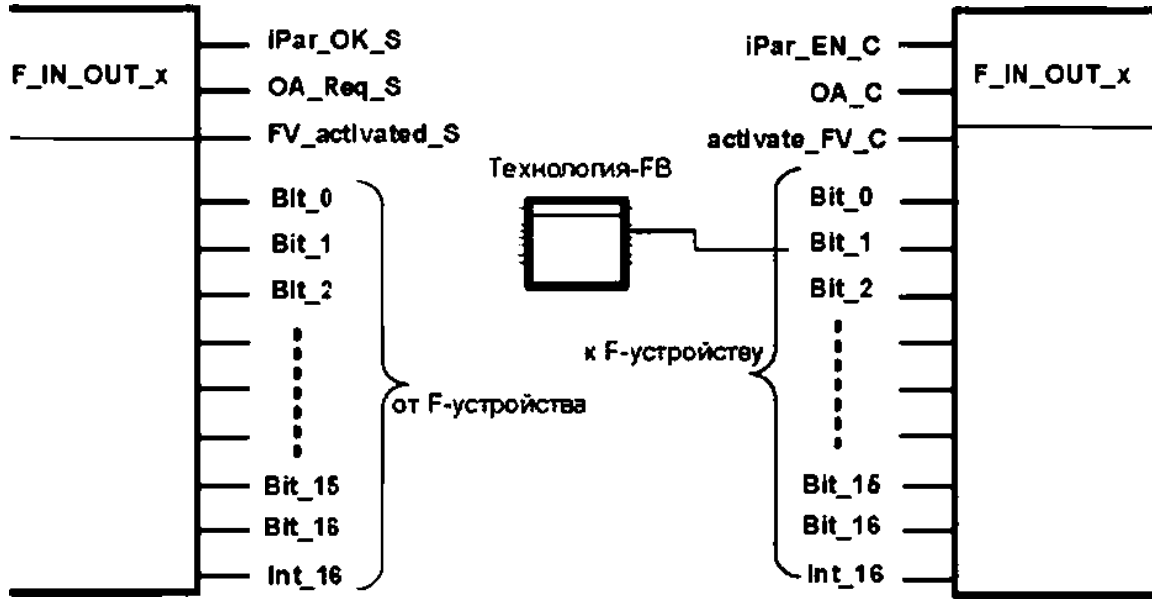
F-

62

F-

F-

61784-3.3—2016



62—

F-

62. ;
 iPar_EN_C — (;
 iPar_OK_S — ;
 — ();
 OA_Req_S — (.CRC,)
 FV_activateO_S — , F- ;
 activate_FV_C — , F- :
 — , « ».
 F

FSCP 3/1.

7.1.3 6.1.
 F- (62).
 F- (Unsigned8), Integer16. Float32 Float32 + Unsigned,
 « ». FV = « »,
 4 (. 7.1.3). FV = « »,
 «activate_FV_C* F-

R7	« -PN/DP»	-
R8	CPF 3 F- API (. R3).	
R9	F- « »	-
R10	« »	
R11	GSD « »	
R12	(Host-Engineering-Tool] « ».	
R13	() CPD-	-
R14	^ F- IO (F- PROXY-FB - FSCP 3/1	
R15	API (cw. R3m R4) CPD-	-
R16	« » CPD- (,HTML)	-
R17	CPD- { «{ » (.R14)	-
R18	CPD- «OSSD1»} * (. GSD 3/RTE	
R19	inap CRC32 26 (inap-cepcep. CPD-)	-

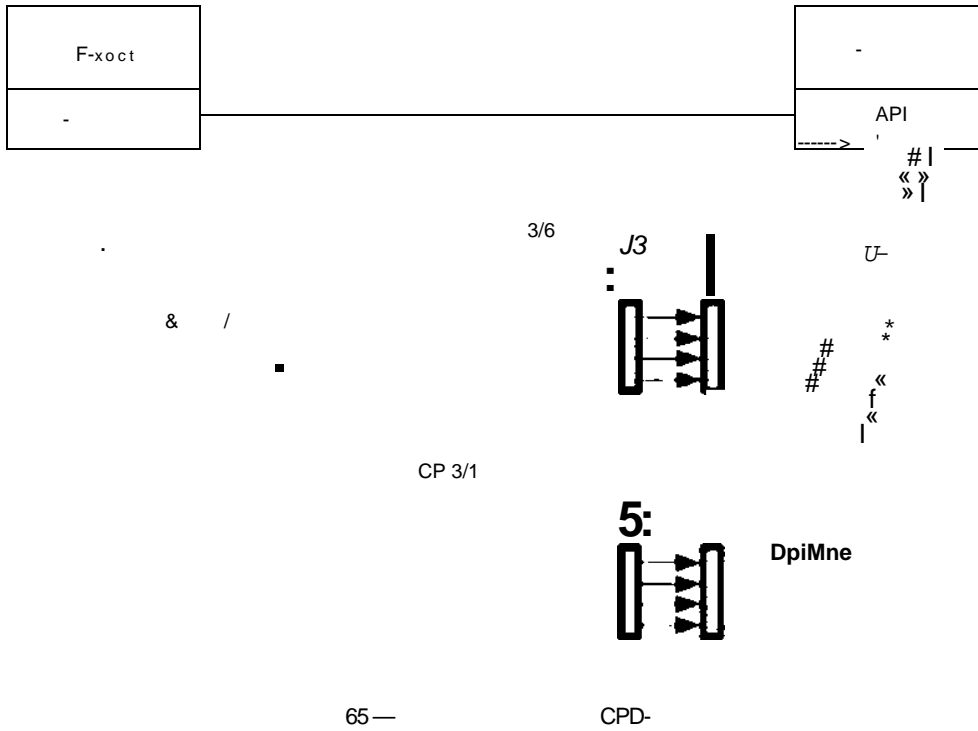
65

CPD (CPD-Tool-Integration).

CPD-

- F- (. USB. RS232);
- F- :
- CP 3/RTE 3/1 :
- 3/1 3/2

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8.6.4
8.6.4.1

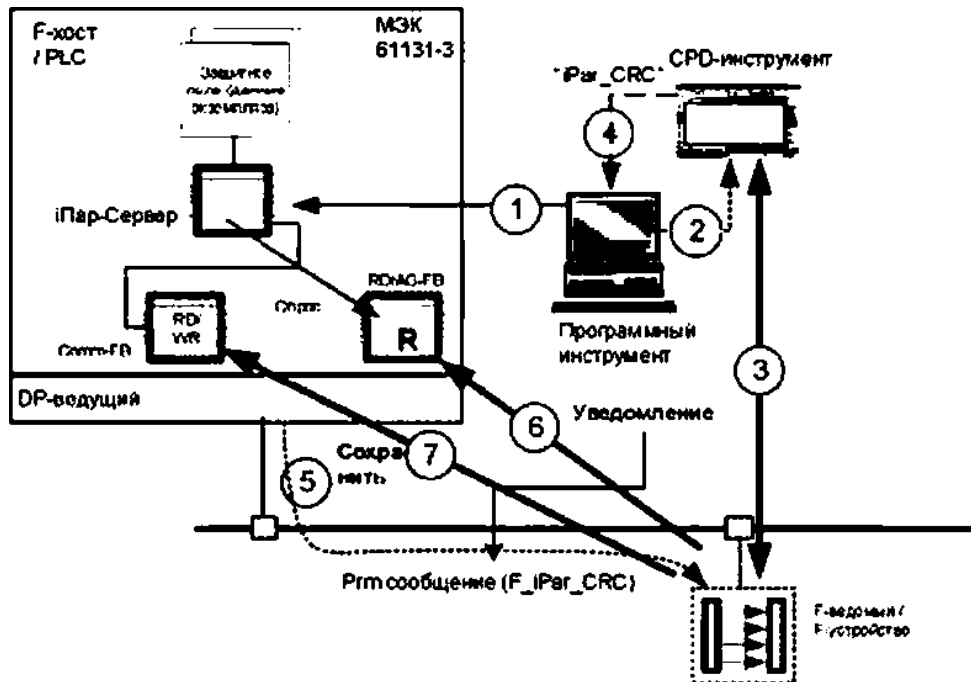
inap-сервера

8.3.1.

23.

F-

PLC.



66 —

inap-сервера ()

66 F- inap-сереера (1). F- / - inap-сереера. (PV-). (2)

iPar_CRC «F_iPar_CRC» (3).

«FJPar_CRC» P- (4). F- / - (5).

(6) inap-сереера.

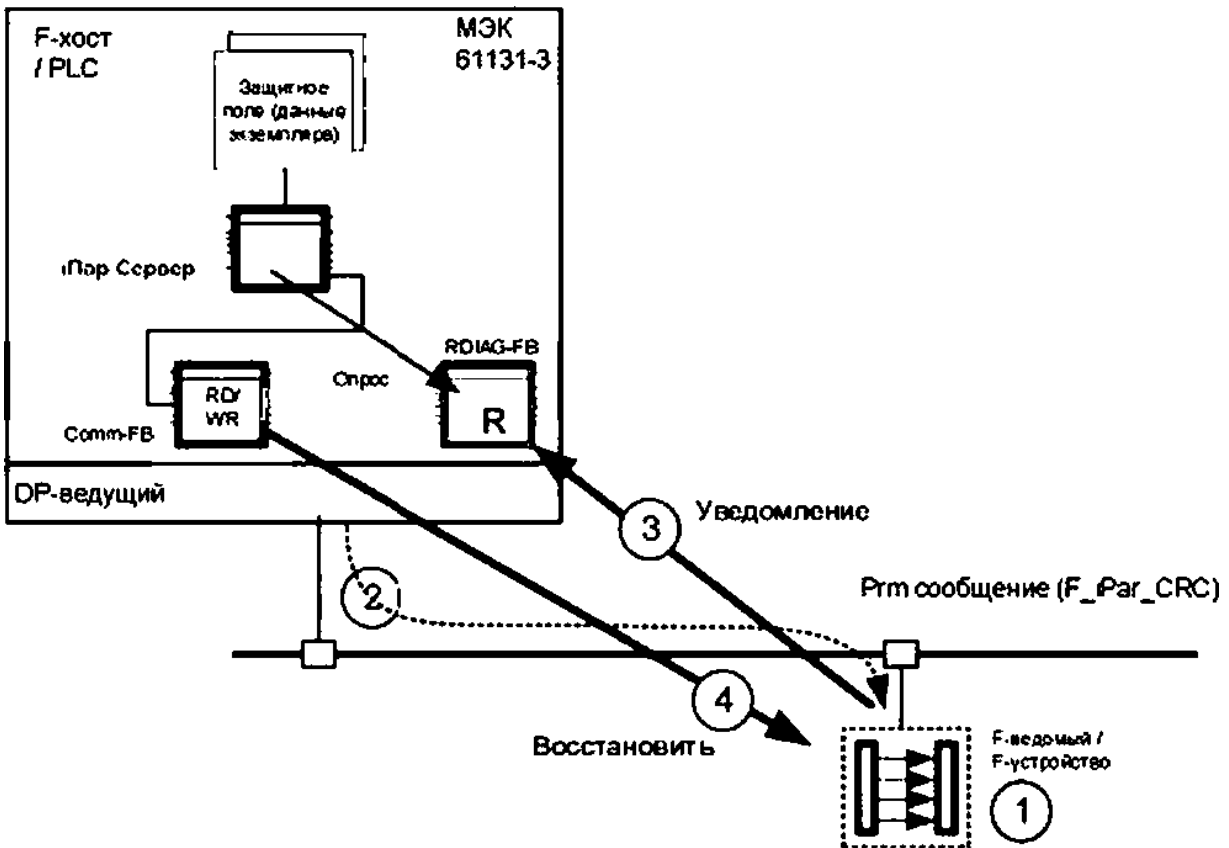
RDIAG CPF 3 (.6.4.2 [49]). inap-сереер (R) (7).

inap-сереера.

67 (1) F- inap-сереера. F- «FJPar_CRC» (2), np/i / - (3) inap-сереера. RDIAG (4).

(8.6.4.2 (49)). inap-сереер (R) (4).

F- CPD



67 — inap-сереера (, F-)

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F_Source/Destination_Address (inap-cepeepa / /): 2¹⁵.1
 - inap-cepeep 52:
 - inap-cepeep F- (67):
 - inap-cepeep V2 FSCP 3/1:
 • F- / - " / - F- / - ; Запрос-inap-cepeepa,
 - « (FV / -)» ().
 8.6.4.2
 CPF 3. F- / - inap-cepee-
 3/1 3/2. 61158-5-3.
 « » [50].
 « inap-cepeepa» (- 7).
 — « » (=6)
 FSCP 3/1 3/2 IO 3/RTE
 68 inap-cepeepa 3/1 3/2.

8	158
»	(6)
2	
3	
4	
S	
0< 6	
-W-Ser-»-*)	— » < MSB - 1
Skxnumtwr	1 (20)
•B*rO iH»f_Hcq_Heoer (UR»fcre<l3h	
•Pari Mw_Seein.Se» <Un»wwl32)	
Per? Ti»n»bi_ir*6w lUncqnnlSZ)	
Pars Toaijibr.see <Vn»gned32i	

68 — inap-cepeepa (« »)

« inap-ceperepa» -
 «Diag.ext.diag» (3) -
 4 61158-5-3
 68. « inap-ceperepa» (7). -
 «0». « inap-ceperepa» ,
 13. I/O , 68.
 F- I/O I/O
 3/1 CP 3/RTE. ,
 F- F-
 («Diag.Ext_Diag_Overflow») [50].
 inap-ceperepa -
 I/O (68 70).

13 — inap-ceperepa

№	Имя	Оформление	2	1	0	Примечания
iParO	iPar_Req_Header	SR_V3fsion	Reserved	N.Count	SR_Type	inap-ceperepa (Unsigned32)
iPar1	Max_Segm_Size	Ox00h	Ox00h	Ox00h	0...234	(Unsigned32)
iPar2	TransferIndex	Ox00h	Ox00h	Ox00h	0...254 (255)	/ (Unsigned32)
«	Total_iPar_Size					1 (Unsigned32)

1 : 7.1.3,
 2 «Max_Segm_Size» 234 , 3/RTE.
 2 -1 FSCP 3/1.
 3 «Transfer_Index» 255 , CALL ()
 4 «Transfer Index» , 255 , 3/RTE
 65 535.
 5 («Total_iPar_Size = 0». inap-
 inapMerpoe.
 6 N.Count (3/1 3/2).
 1 15

«SR.Version» e0x01h. «N_Count» 15
 «1» (3/1 3/2)
 «1». «SR_Type» ,
 69.

61784-3.3—2016

1	5	4			0
---	---	---	--	--	---

• 0 0 . « .
 • 0 »)
 • • 3 0 < }1 . **
 • • « 1 1 ') '* *>«1
 • - t_ «< < » 71 .
 • • 0 « • • « * « - - ->
 • 4 • * * moriw.hh puMput
 . 1 OMCUUM

69— SR_Type

[49]

RDIAG.

F- .

4.4) inap-ceepepa F- «Save» (/ -) «Restore» (21* (-)).

6.3.2.

CP 3/RTE

inap-ceepepa

« 61158-5-10 » «Upload&Retrieval» inap-ceepepa
 ceepepa CP 3/RTE (61158-6-10. 70 inap-ceepepa - / - inap-
 218 (4.4) « »

« ». 6.3.2. inap-ceepepa « »
 . inap- «0».

» »

CVtH 1	1	1 (Notification (VJpted&Reineval - •)
ami	1	
*	1	
4	1	
(«	1	
-	1	
API	4	> (52 6f»0
**	?	
liMH	?	
»	?	
»1> 1 » »	4	
	7	
	>	
— »	0	
	1	
	J	
** ».«* .**«**	4	
	4	
? IUHtfMgl	4	
ftfj IUHspWX;	4	

70— inap- (« »)

8.6.4.3
inap-cepeep

« » « » (read record) « » (write record),
61158-5-3.
« » « » (14 15).
61131-3. [49]
ROREC WRREC.
F-
14— Read_RES_PDU (« »)

Read_RES_PDU				
FunctionJMum	1	0 5	«Read», fix	
Slot.Number	1	0... 2 5 5		
Index	1	0... 2 5 4	"Transfejindex"	
Length of net data	1	0... 2 4 0	(
iParameter ()			= 240	

CP 3/RTE [49].

15— Write_REQ_PDU (« »)

Write_REQ_PDU				
Function_Num	1	0x5F	«Write», fix	
Slot_Number	1	0... 2 5 5		
Index	1	0... 2 5 4	Transferjindex"	
Length of net data	1	0... 240	inap	
iParameter			= 240	

{ , F- «
/F « », 61158-5-3 «Pull» «Push» (
) , 16 17.

16— Pull_RES_PDU («Pull»)

PuR.RES.PDU				
Function_Num	1	0 5	«Read», fix	
Slot_Number	1	0... 2 5 5		
index	1	0... 254 (255)	"ransferjindex')	
Length of net data	1	0... 2 4 0	inap + -	
Extended_Function_Num	1	0x02	«Puli»	
Options	1	Unsigned8	5-3. 6.2.17.2 , . 61158-	
Sequence.Number	4	Unsigned32	... «	
iParameter ()			= 240	

al «Transfer Jndex* 255 61158-5-3.

CALL

« » «Push».

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17 — Push_REQ_PDU («Push»)

Pull_RES_PDU				
Function_Num	1	0x5F	«Write», fix	
Stot_Number	1	0... 255		
Index	1	0... 254 (255)	«TransferIndex» ^a	
Length of net data	1	0... 240	! +	
Extended_Function_Num		0x01	«Push»	
Options	1	Unsigned8	61158-5-3.6.2.17.2	
Sequence_Number	4	Unsigned32	... iPar	
iParameter ()		Octet String	= 240	

⁴¹ «Transfer Index» 255

61158-5-3.

CAL.

«Pull» «Push».

F-
inap-cepeep

«Uptoad&Retneval» (61784*2 (CP 3/RTE).

) CP3/RTE.

61158-5-10.

61158-6-10

F-

I/O

3/1

I/O CP 3/RTE.

I/O

« »

« » « »

CRC.

8.6.4.4

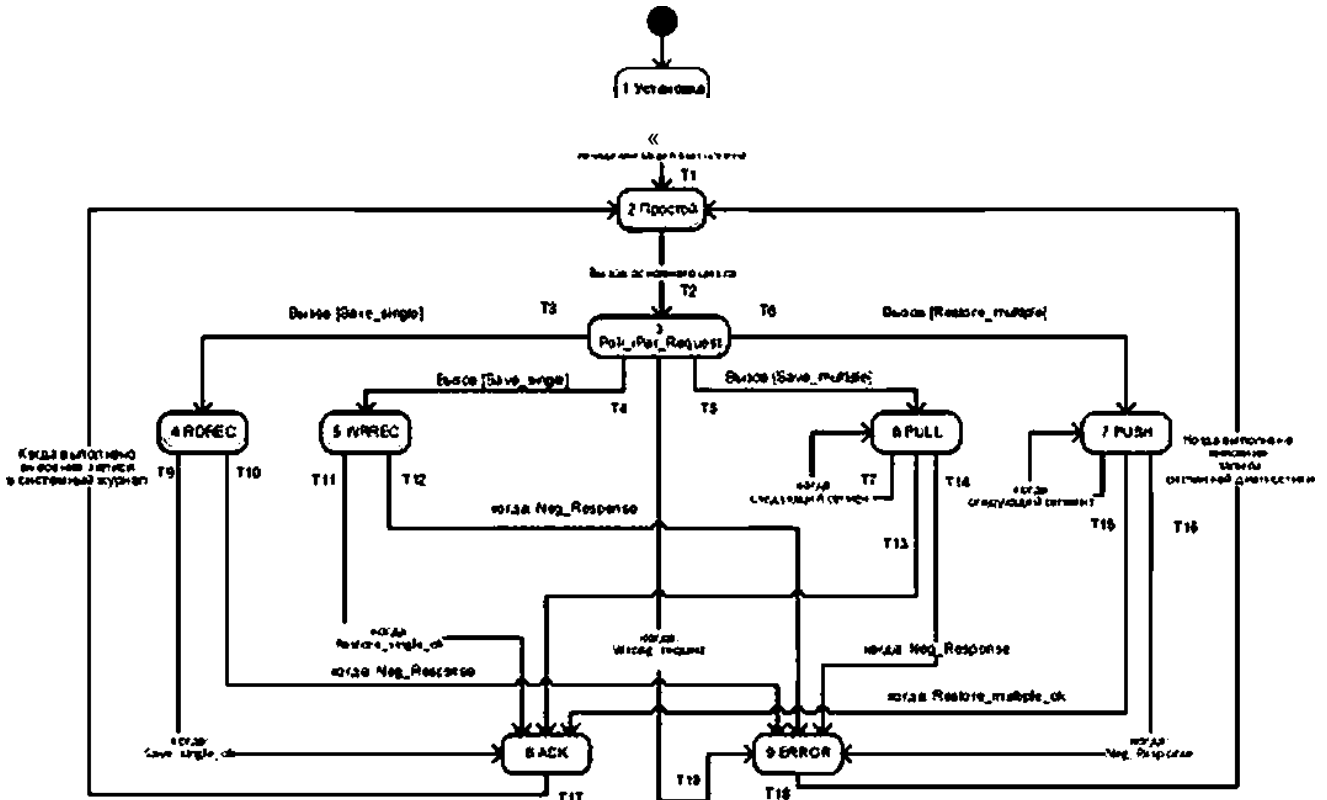
71

inap-

18

UML2 . 7.2.2.

inap*



71 — i -

	71	:	
Save_single	—	inap-cepeepa	()
		(RDREC);	
Restore_single	—	inap-cepeepa	()
		(WRREC);	
Save_mu1tiple	—	inap-cepeepa	()
		(PULL);	
Restore_multiple	—	inap-cepeepa	()
		(PUSH);	
System log entry ()	—	inap-cepeepa ()	:
System diagnosis entry (-)	—		
	:		
Neg_Response	—	PUSH	, RDREC. WRREC. PULL
			, inap-
			;
Wrong_request	—	inap-cepeep	

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18 — inap-cepsepa

1. Initialisation (())	h&ie : ,
2 Idle ()	.
3 Poll_iPar_Request	inap-cepsepa ERROR
4 RDREC	RDREC ((49)) 3/1 3/2 61158-5-3
5 WRREC	WRREC ((49)) 3/1 3/2 61158-5-3
6 PULL	PULL, - «Extended Function Num» = 0 028 /1 3/2 61158-5-3
7 PUSH	PULL, - «Extended_Funct»on_Num» = 0x01 - 3/1 3/2 61158-5-3
SACK	« - »()
9 ERROR	RDREC. WRREC. PULL, PUSH - , i -

18

1	1	2	-
2	2	3) (
	3	4	RDREC (
4	3	5	WRREC (
5	3	6	POLL - (
	3	7	PUSH - (
7	6	6	
8	7	7	
T9	4	8	RDREC ()
10	4	9	
11	5	8	WRREC ()
12	5	9	
13	6	8	POLL ()
14	6	9	

18

15	7	8	PUSH ()
16	7	9	
17	8	2	{Idle}
18	9	2	(Idle)
19	3	9	

8.6.4.5

inap-

19

inap-

F- . inap-cepcep { F- . F-
 19— inap-

!		
F_S/D_Address	8.1.2 7.3.7 9.1	F_Sourc / Destination_Address F_SD_Address { F_S/D_Address iPar_CRC { F_S/D_Address F_iPar_CRC F_S/D_Address , iPar_CRC FjPar_CRC. F_S/D_Address. - F_S/D_Address - F_S/D_Address -
	8.1.7 8.6.3 9.1	F- , F- F- F-na- CPF 3 { }. F_iPar_CRC <0>. FV (), 21 ()
		- MS2. - FSCP CPD-
iPar_CRC / FjPar_CRC	8.2 8.3. 2	iPar_CRC FjPar_CRC. - { iPar_CRC CPD- «0>. «1>. F- iPar_CRC F- F- F_iPar_CRC. -
-	8.1.7	iPar_CRC CPD- - «F_iPar_CRC» -
	8.2	{ ()

61784-3.3—2016

19

/		
	8.2), «HW release» (), «SW release» (), «order number» ()
		, (« » « F_iPar_CRC
inap-сераер		F- . F- F- (F-) inap-
	9.1	F- . F- F- FSCP, (9.1. 2

8.6.4.6 (GSD F- . F- F- «Max_iParameter_Size» GSO {«MaxJParameterSize» GSDML). [43] [47].

9

9.1

1 « F- , F- (=1). : ; (F-). (« ,), 0.5 (« , , « _ »).

F- FSCP F- . F- (« F-) F_S/D_Address F- - 2 7.3.7 8.1.2

9.2

61784-5-3.

61918

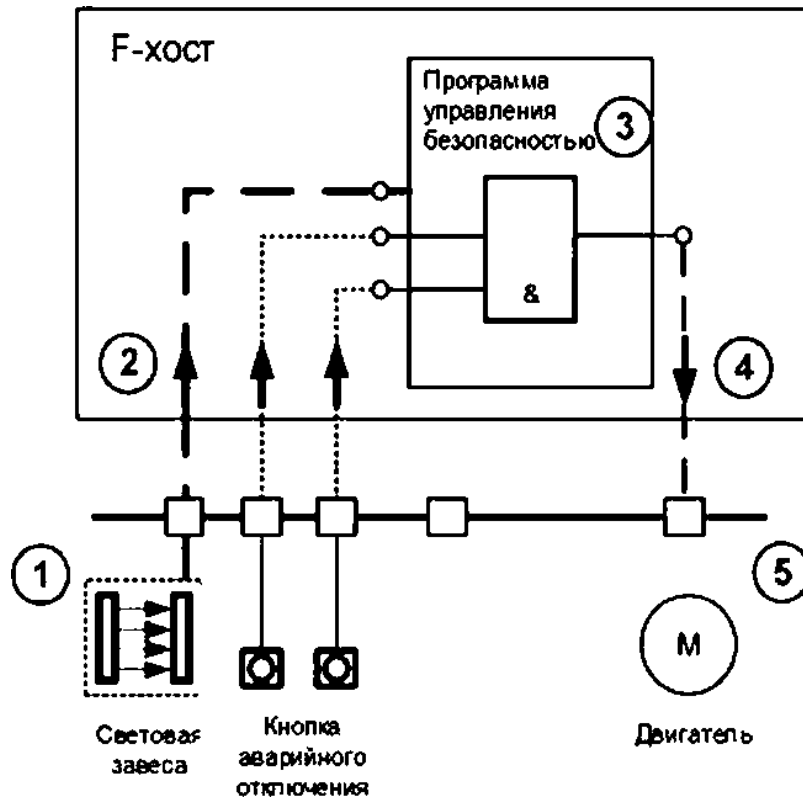
[44].

CPF 3

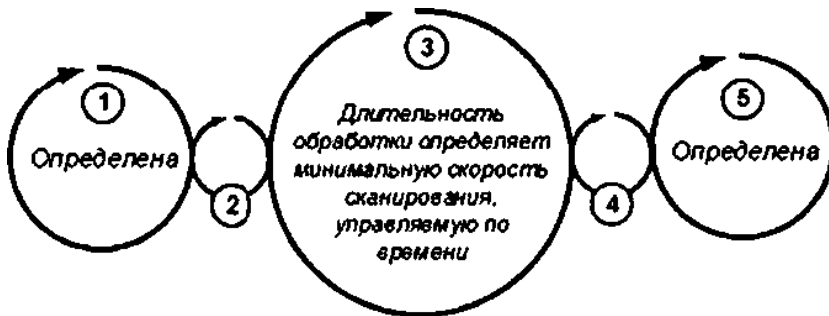
9.3

9.3.1

, F- , (72).



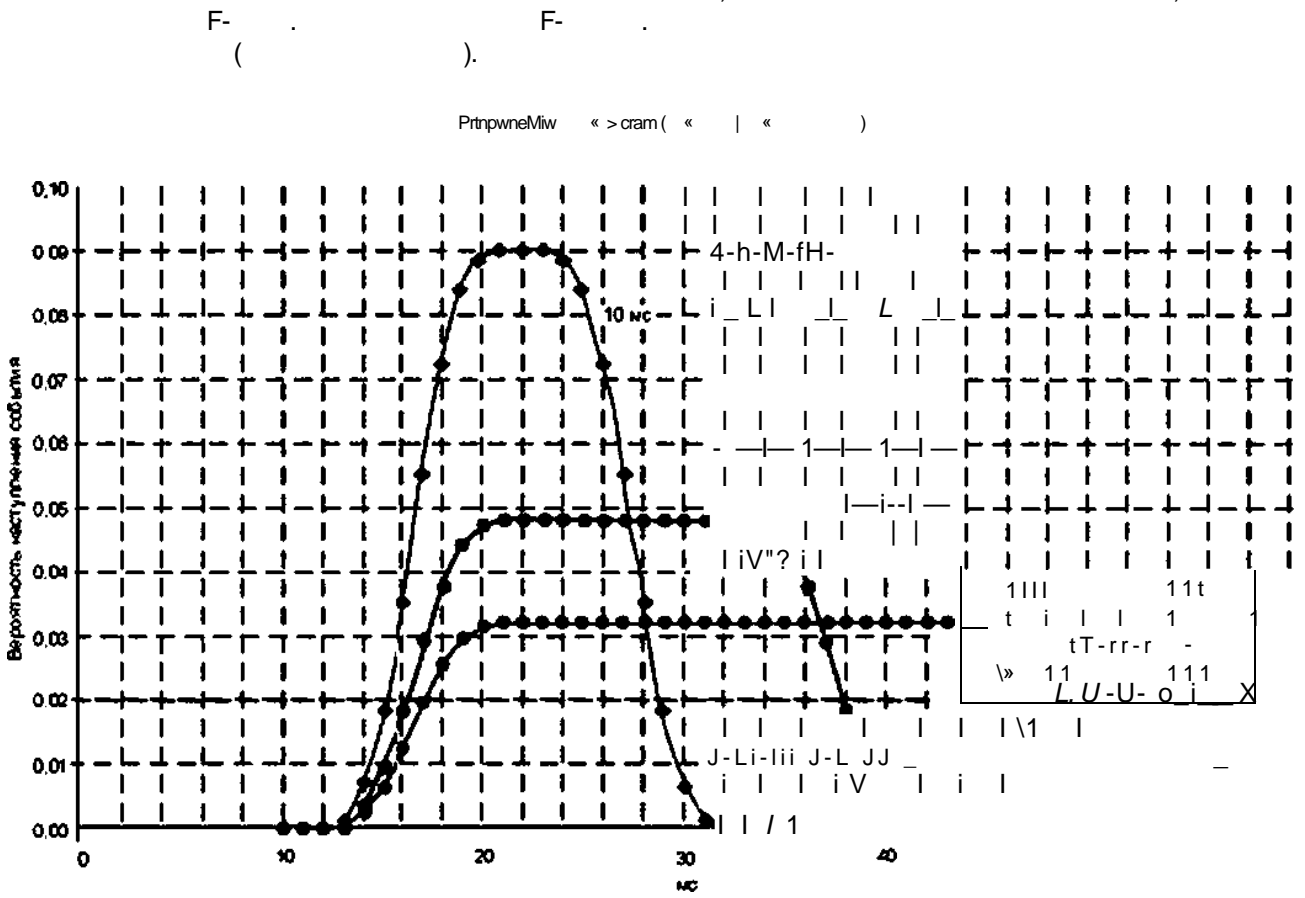
73. ()



3.	1.	5.	1.	3.
4.	2	6.	2	4.
5.		7.		5,
		15		6

*) 5 . «

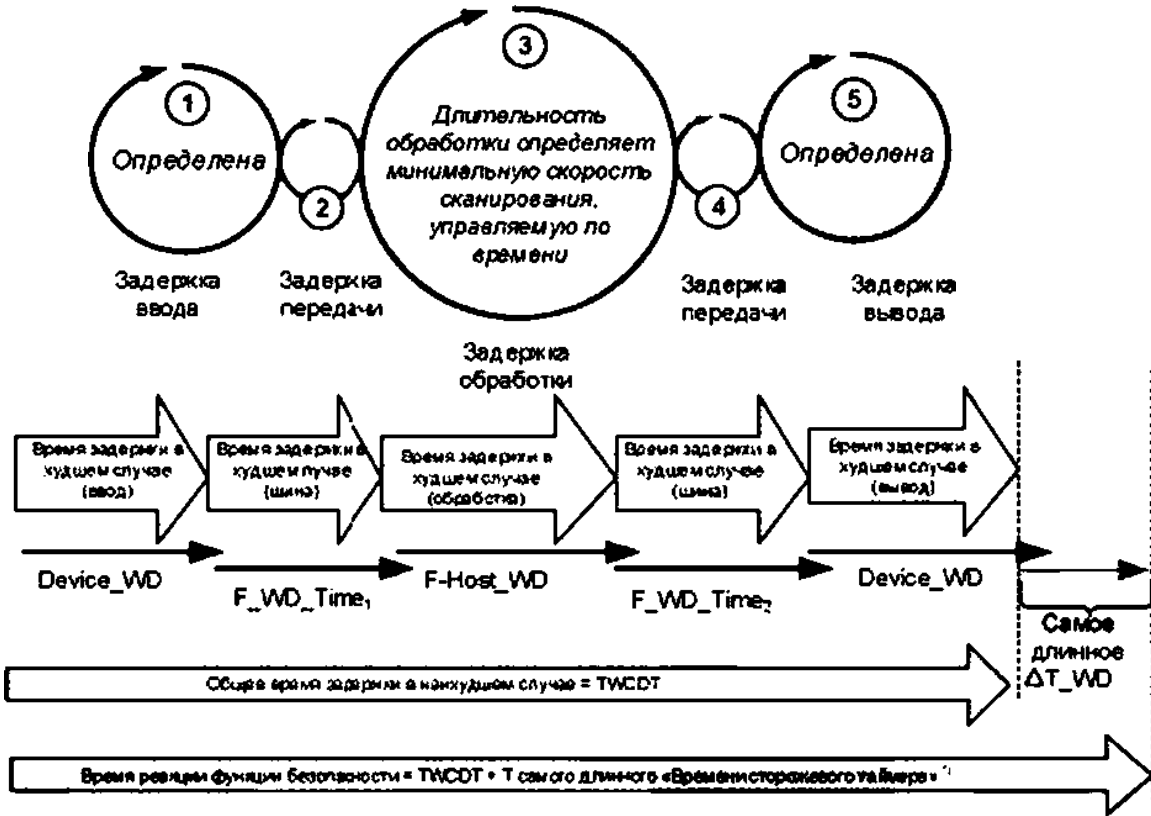
61784-3.3—2016



74—

) () ()
, F- ()
, 5
10 5
74 15 . 8 , 13 .
10 .20 30 .
9.3.2
9.3.1
(WCDT,).
(WDTIMEj,

75



*)

(WDⁿme).

(1)

$$SFRT \leq WCDT * \max_{2n}(WDTIME, WCDT),$$

SFRT

WCDT,

WOTIME, — WDTIME

PDU

F_WDⁿme.

$$\begin{aligned}
 & \text{— TD1: } OFDT_{Input} \quad WCDT_{T01} + T_{cy_{F>W4}}; \\
 & \text{— F-xoct: } of dt_{fimi}; \\
 & \text{— TD2: } F_WD_Time2 \quad WCDT_{T02} \quad DAT_{Ou(pu)}; \\
 & \text{— } OFDT_{ouipoi};
 \end{aligned}$$

OFDT

T_{cy-F_{es}}(—

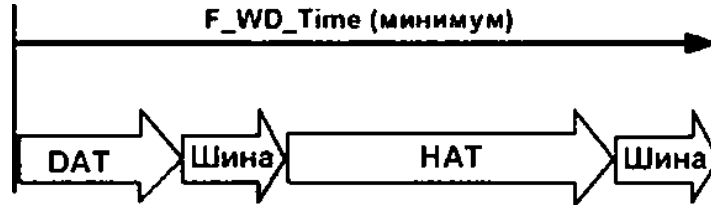
F-

61784-3.3—2016

9.3.3

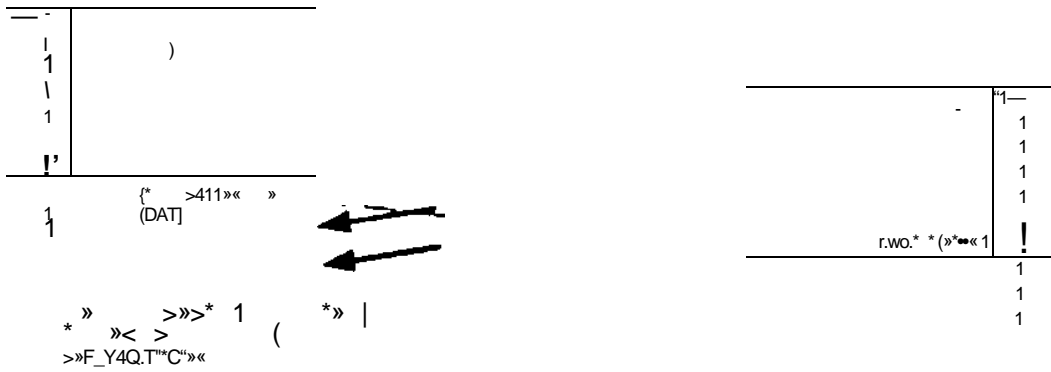
FSCP 3/1

F- 1:1 F_WD_Time FSCP3/1 (8.1.3) 76 (DAT — — HAT —). F- F- (6.2) (FSCP 3/1), F- F- PDU (FSCP 3/1, PDU «DAT = », F- DAT PDU F- F- FSCP 3/1. PDU PDU [* 1). PDU « F- (). F-нара



F-

F-хост



76—

F_WD_T*me

FSCP 3/1

8.1.3 F_WD_Time. 73 ((6) =10 (15). F_WD_Time = 4 ms + 6 ms (2*2) DAT (6)

2 3/1) 9.3.5. ,
 (1) 9.3.2 DAT. HAT F- F_WD_Time ,
 DAT. HAT ,
 30 %.

9.3.4

9.3.5 ()

— f —
 H-l-t-^+ 4-F4-b4-r
 0 < :
 <<<! { =10);
 «fv+etoi*• * Stop
 1—
 — 1—| — •—|— —1—1—|—|—
 |||||
 —|—|—|—4—|—4—|—|—1—|—|—L_1_J—|—|—
 11 | 11/ 1/111 | 1,1
 4--|—U4-1- J#_1_4-1 -L-i-|
 11 | 1 V / | | | 1;1
 1J | 1' /111 1it1
 11 | 11 / || t |
 11 | 11 h iii 11.1
 " 7' " "1
 ||/ | | | |
 .+^#.+—4-4-
 | || ||
 FL_—1—|—L_1.
 1 1 1 1
 | | | |
 2 0 49

77—

78

3/RTE.

3/1,

79

61784-3.3—2016

3/1

* 1

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3/6

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FSCP3/1

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FSCP3/1

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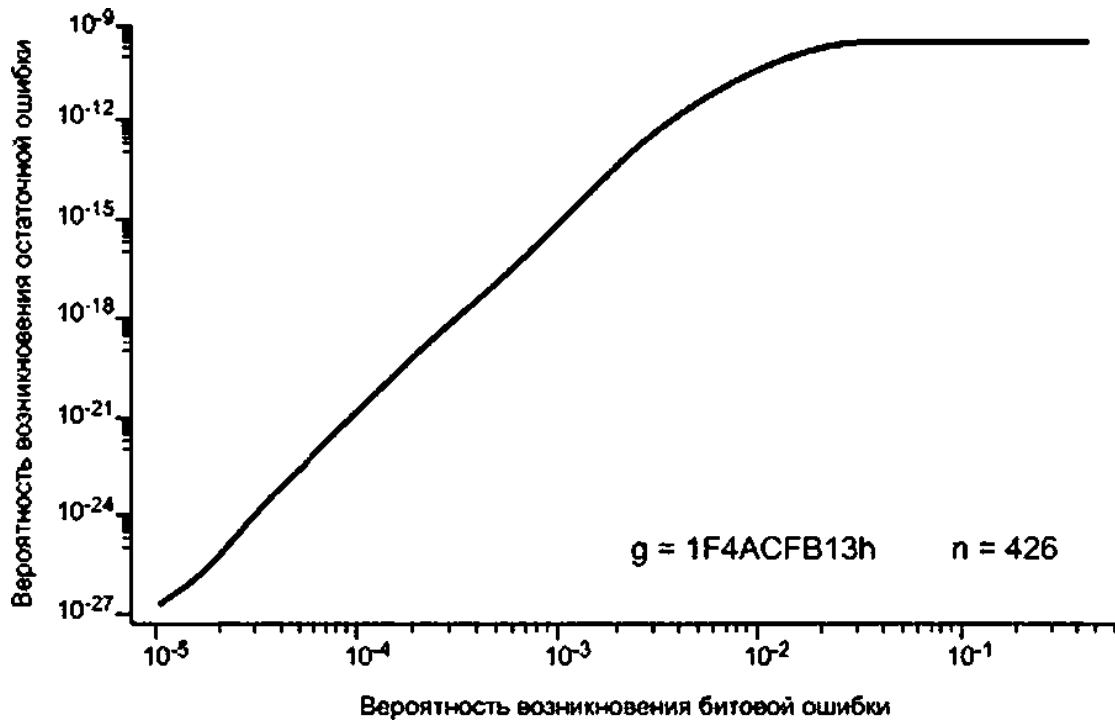
79—

3/RTE

61784-3.3—2016

81 82

32-



81 —

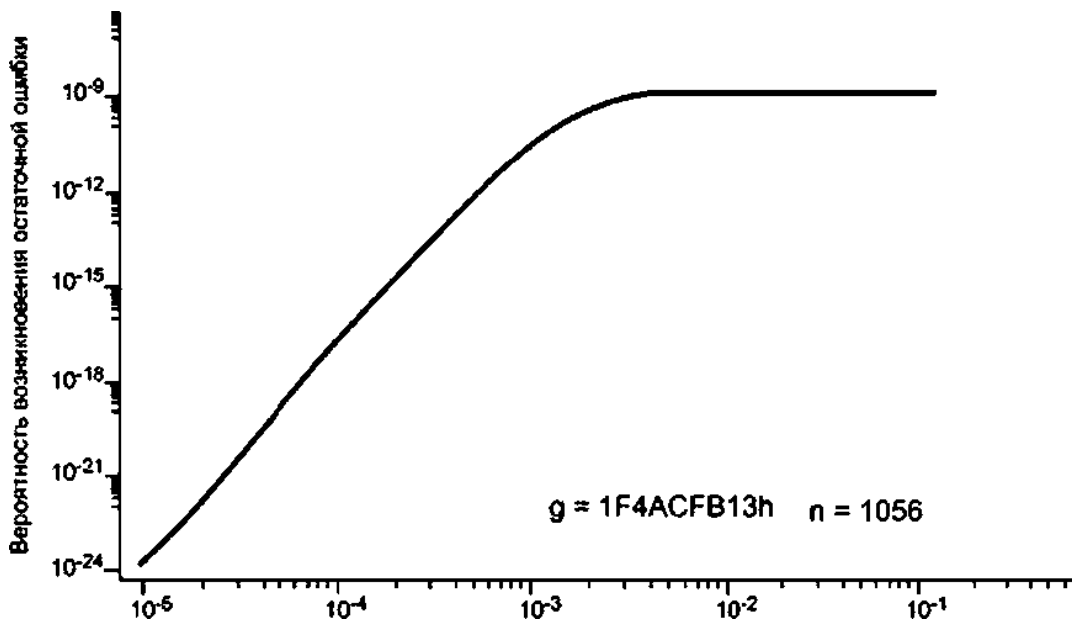
32-

52

81 82.

1F4ACFB13h:

CRC.

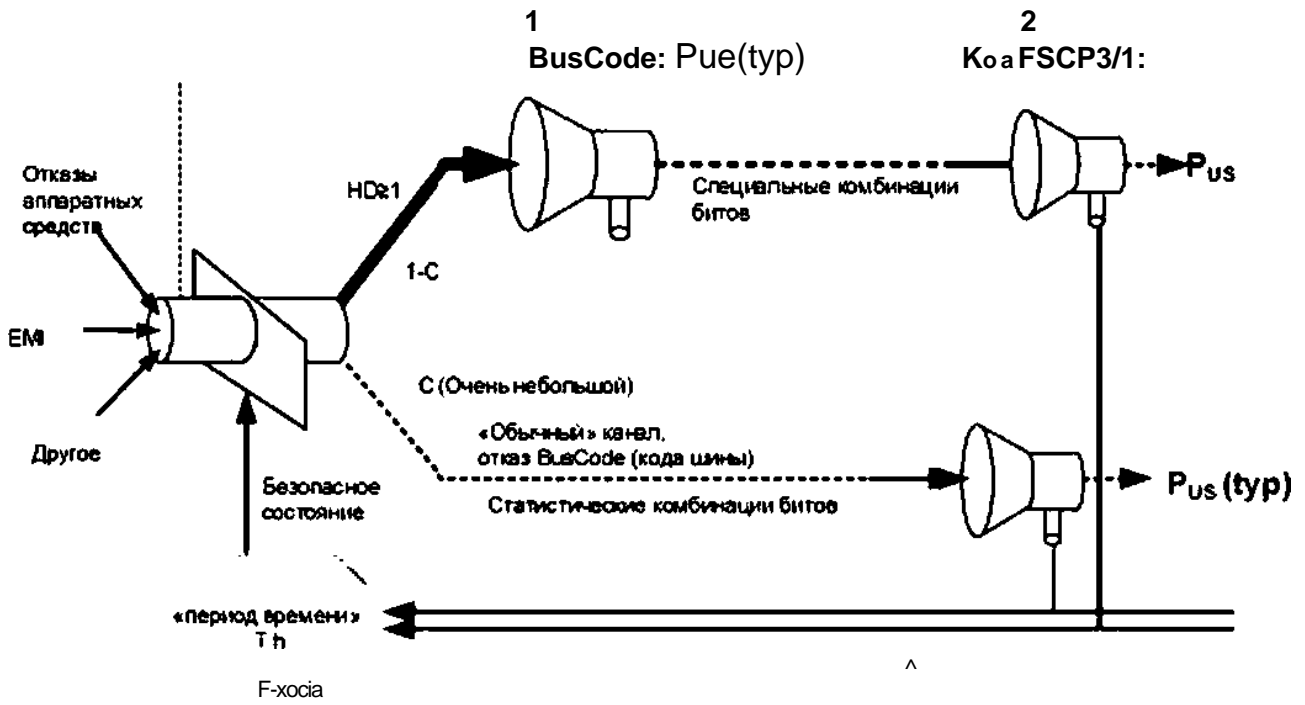


52 —

32-

132

PDU



63—

83,

fw —

HD —

—

—

(.7.2.6).

10

3/1

CP 3/RTE

(

),

FSCP 3/1

F-

, PDU

F-

PDU

9.5.2

•

CPF 3.

SELV/PELVAAfi

•

61000-6-2

61326-3-1

61131-2

61326-3-2.

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V1.

1:1 FSCP 3/1:
 3/1: 100:
 3/2: 10.
 (.9.3.5):
 /1: 15 (61158-6-3: 8);
 3/2: 15 (61158-6-3: 8);
 : 8 ().
 - CRC : CRC 14EABh 1F4ACF813h;
 C599h.
 3/1: 2 /
 - PDU :

V2.

1:1 FSCP 3/1 < 10 000.
 (.9.3.5):
 - CRC :
 (.7.3.8 5.4.2).
 (.7.3.9).
 • PDU :
 9.5.3 ()
 (). PDU ().
 /1:
 3/RTE: F->.oct
 Ethernet
 61131-2.

248.

9.6
 9.6.1 / F-
 F-
 v (_).
 9.6.2 ()

F- CPF 3

i , 8.2. F- (46) /

9.7 F- / IM4 ()
 F- /
 F-
 61508-2. F- F- -
 FSCP 3/1
 20.
 20 —

	61508	9.1 () 7.3.7 (F-)
	(PELV).	(44)
	61010-1 61131-2(. 1 /)	(44)
	()	62061. 61326-3-1 61326-3-2 - 61496 (6); [44]
		[44]
		7.3.8. 7.3.9.9.5.2. 9.5.3
	61918 61784-5-3	[64]
	61764-5-3. « . . »	[65]
	F_iPar_CRC > «0»	8.6.4.5
	:	9.6
	()	61508
	DAT. WCDT. WDTcme	9.3.2 9.3.3
()	: PFH (-)	62061
()). MTTFd(PL ()	1 013849-
	: PFD (-) ,	61511 [30]
		9.8. [44]. [53]
		[45] 10

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9.8

9.8.1

FSCP 3/1

10².

9.8.2

9.8.3

FSCP 3/1

[44].

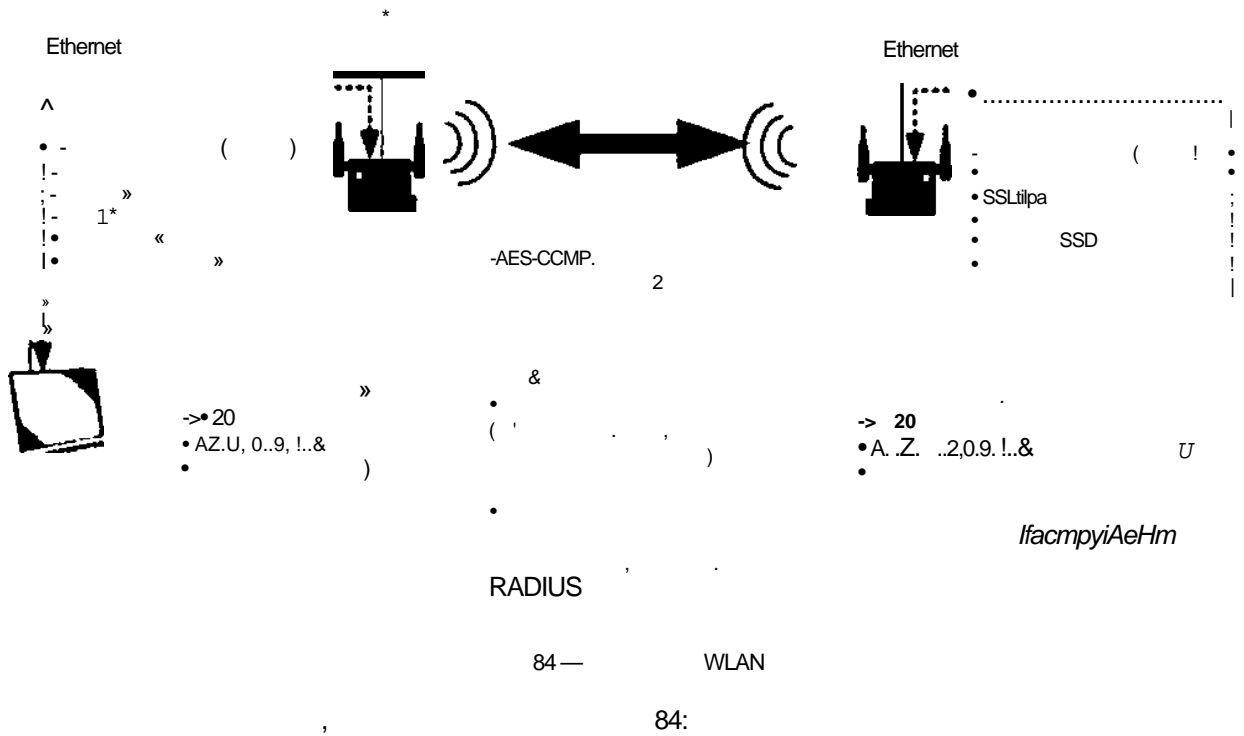
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802.11i (26)
84

WLAN,

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AES-CCMP —

RADIUS —

SSID —

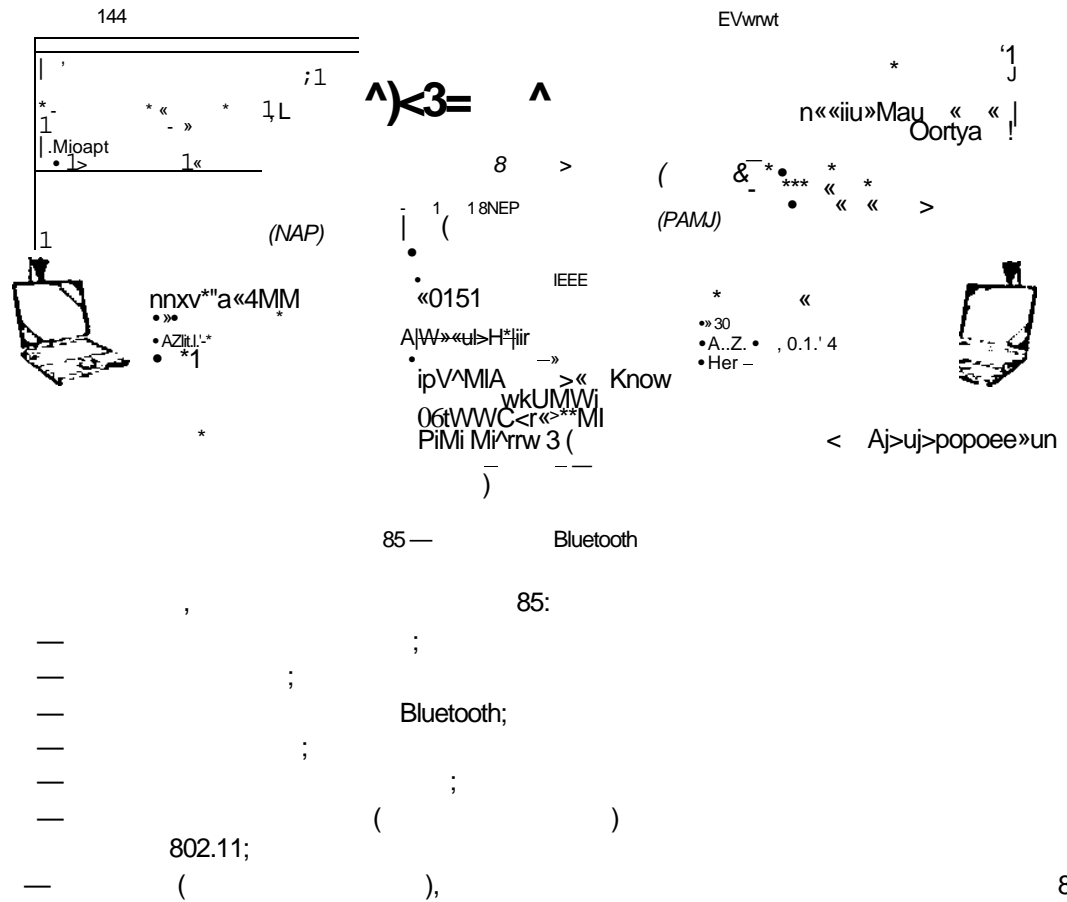
SSL —

WPA2 — Wi-Fi 2 (802.11i (26]):
 — 802.11;
 — , 802.11.

21 — WLAN (802.11i)

1		/ SSL https. -
2		20 . -
3		. -
4) ((- , . RADIUS). (- , -
5) (-
6		20 (. (26) .4 -) .
7	(PDU)	AES-CCMP (WPA2) [26] -
8	SSID	SSID , SSID . SSID
1		. ,
2		. .

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22 —

Bluetooth (802.15.1)

1		SSL https. /
2		16 .
3	
4		Bluetooth 3(-) , 802.15.1 . (PIN). 1
5		16 .
6	(PDU)	802.15.1

Nt		
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9.8.4

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(, , . 10218-1 (22)).

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(F-), (). FSCP 3/1.
23.

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F-

GSD	V5.04 (PB-DP) [43J; V2.0 (PN-IO) (47)		F-
61131-3	: RDREC, WRREC. RD1AG, RALRM (49)		MS1 : GETIO.PART. SETIO.PART
iflap-cepeep	« - » 2 ¹⁵		: RDREC. WRREC. RDIAG (RALRM) (49]
()	64 (-), Unsigned [^] . -16. -32		-
()	12 1/0		
	Unsigned8. -18. -32. Integer16. -32. Real (Float)	FSCP 3/1: Unsigned8.-16.-32. Integer16. -32. Float32. Unsigned8+Unsigned8, Float32+Unsigned8	FSCP 3/1 F-
F-хосра			

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23

	*		
	: (6.3.2)	: (6.32)	[50]
MSI			3/1
MS2	(PLC)		CPU -
	3(CNC; 2)	3	
	- 12	[63] - 12	

10

10.1

FSCP 3/1

61508. 61511. 60204-1. 62061. 13649-2) /

[66].

10.2

fix Arbeitsschutz / BG — « » BGI(Aberufsgenossenschaftliches Institut
^ HSE ()
FM (Factory Mutual /
) UL (Underwriters Laboratories Inc. /
) or the INRS (Institut National de Recherche et de Sécurité)

FSCP 3/1 61784-3.
[45].

()

CPF 3

.1

24- CRC ; 15D6DCBh ;
CRC

```
void crc24_calc(unsigned char *p, unsigned long *x)
{
    int i;
    for (i = 1; i <= 8; i++)
        if ((*p & 0x800000) != (bool)*x & 0x80)
            XOR = 1;
        *p = (*p << 1) ^ 0x5D6DCB;
        else
            XOR = 0; // pure shift 1
        *p = (*p << 1);
        *x = (*x << 1);
    for (i = 0; i < V; i++)

```

A.1 — « »

CRC 24- CRC

(.1)

:

= cxtab24 [((*g+) & 0xff] (« 8).

(.1)

24- CRC ;

q++:

CRC.

0».

.1- « 1 24

24- CRC

.1— (24»

24- CRC

CRC (0...255)

0x000000	0X5D6DCB	0 0 9	0 7 65	0 28 7	0 75 72	0x920171	
0x51 5	0X0CD805	0 6 58		0X796F29	0 2402 2	0XC3B4BF	0 9 974
	0XFE0657	0 19 03	0x44 DDC1	0 8 17		0X316AED	0 6 0726
0XF2DES2	0xAFB399	0 4805 4	0X15680F	0 04 5	0 87697	0x600F 23	0X3DB2E8
	0x460738	0 16155	0XFCOAE	0x336014	0X6E0DDF	0 89 82	0x040649
0X4A0F3D	0X1762F6	0XF0D4AB		0X62D5DA	0X3FB811	0 80 4	0x856387
0XB8D16F	0 5 4	0 020 =9	0X5F6732	900 88	0XCD6643	0x2 A DO 1	0X77BDD5
0 964 1	0 4096	0X53BF37	0X0ED2FC	0 1 46	0x900380	0x766500	0x26081
0 3775 6	0X6A182D	0 8 70		0X1FAF01	0 42 2	0 57497	0XF8195C
0 66 028		0XDC1BBE	0x817675	0 4 1 ACF	0x137704	0XF4C159	0 9 92

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.1

CRC(0...255)							
0x941 7	0 973 1	0 2 5	0 73 827	0x800490	0 1 956	0X061F0B	0x587200
0 5 4	0X98C67F	0X7F7022	0x221DE9	0XED7153	0 01 98	0 57 5	0 0 70
0X2CCF15	0x71 A2DE	0x961483	0 7948	0x0415F2	0x597839	0 64	0XE3A3AF
0X7D7ADB	0x201710	0XC7A14D	0 9 86	0 55 0	0X08CDF7	0XEF7BAA	0 21661
0X8FA489	0XD2C942	0X357F1F	0x681204	0 77 6	0XFA13A5	0X1DA5F8	0x400833
1147	0 837 8	0 64 1	0 39 71	0XF6CBA0		0x401036	0X117DFD
	0x338607	0XD4305A	0x895091	0x46312	0X1B5CE0	0xFCEABD	0 18776
0X3F5E02	0 6233 9	0x858594	0XD8E85F	0 1784 5	0 4 92	0XAD5F73	0XF032B8
		0 775 6	0X2A360D	0 55 7	0 8377	0X5F8121	0 02
0 9 359	0 15855	0 26 08	0 7 83	0XB4EF79	0 982 2	0X0E34EF	0x535924
0X75513F	0X283CF4	0XCF8AA9	0 92 762	0x508608	0 00 613	0 7504	0XBA3D85
0X24E4F1	0 79693	0X9E3F67	0 352	0 0 16	0x515300	0 6 580	0 884
	0 8 5768	0 6 135	0X318CFE	0XFEE044	0XA38D8F	0X443BD2	0x195619
0X878F6D	0XDAE2A6	0x3054F	0x603930	0XAF5S8A	0XF23841	0 158 1	0X48E3D7
0 599 2	0X04F3E1	0 545	0 2877	0x714400	0x202906	0XCB9F5B	0X96F290
082 4	0X55462F	0XB2F072	0XEF9DB9	0X20F103	0x709008	0 9 2 95	0 7475
0XF4FSB6	0XA7987D	402 20	0X1D43EB	0XD22F51	0X8F429A	0X68F4C7	0x359900
0 4078	0XF62D83	0 119	0X4CF625	0X839A9F	0XDEF754	0x394109	0 642 2
0x422409	0X1F4912	0XF8FF4F	0 59284		0X3793F5	0 025 8	0x804863
0x139117	0X4EFCDC	0 94 81	0XF4274A	0X3B4BF0	0x66263	0x819066	0xDCFDAD
0XE14F45	0 228	0X5B94D3	0X06F918	0 995 2	0x94F869	0 734 34	0x2E23FF
0XB0FA8B	0XED9740	0 0 2110	0x5740 06	0x962060	0XC54DA7	0X22FBFA	0x7F9631
	— 1			24-			(0...255)
(255).		1 24().				(0)	

iHGinq (.1) 32- CRC
 = 1 32 [((» 24) *q++) & 0xff] (« 8). (.2)
 2.

.2 — « 1 32» 32- CRC

CRC (0...255)							
0000000	F4ACFB13	1DF50D35	E959F626	1 6	CF46E179	261F175F	02 4
77043404	8378CFC7	6 2139 1	9E80C2F2	4 2	B892D5AD	51 238	A567D898
EFA869A8	1 0492 8	F2506490	06F19F8E	D44273C2	20EE88D1	C9B77EF7	301 85 4
987C5D7C	6COOA66F	85895049	7125 5	3964716	573 05	634 23	4ACFB130
2BFC2843	OF50O350	36092576	2 50 65	10163229	4 93	0DE33F1C	F94FC40F
50281 97	884 784	41DD11A2	571 1	67C206FD	936EFDEE	7 370 8	8E9BFOOB
45441	30F6BAF8	09 *4 0	2D0DB7CD	FFBE5B81	0812 092	24 56 4	16E7ADA7
B380753F	472 8 2	75780	5 098 19	886A6F55	7 69446	959F6260	61339973
57F85086	354 95	4A0D5DB3	1 6 0	6 124	98BEB1FF	71E747D9	854
202 6452	D4809F41	30096967	9759274	1 67 38	EF6A852B	06337300	F29F881E

.2

CRC (0...255)							
850392	4CFCC23D	5 5341	5109CF08	838 2344	7716D857	9E4F2E71	6 30562
CF840DFA	3B28F6E9	D27100CF	26DDFBDC	F46E1790	2 83	99 1 5	1D37E1B6
7 0478 5	88A883D6	61F175F0	95508	47EE62AF	34299	5A1B6F9A	79489
06004 11	FF7CB702	16254124	289 37	303 567	C496AD6B	20CF5B4E	D963A050
93AC116D	6700 7	8 591 58	7AF5E74B	8460 07	5CEAF014	5 30632	411FFD21
47825 9	1004DEAA	F98D283C	0021D39F	DF923FD3	2 4 0	26732 6	36CBC9F5
AFF0A10C	5B5C5A1F	205 39	46 9572	941 66	60 64075	89EFB653	7D434D40
08249508	2 886	C5D198ED	317D63FE	E3CE8FB2	176274 1	FE3B8287	0 977994
4058 4	B4F433B7	50ADCS91	9013 82	7BB2D2CE	8F1E29DO	6647DFFB	92 24 8
378CFC70	3200763	2A79F145	DED50A56	0 66 61	F8CA1D09	1193EB2F	E53F103C
840C894F	70 0725	99F984TA	6D557F69	BFE69325	4 4 6836	2139 10	56BF6503
F3D8BD98	07744688	EE2DB0AE	1A814BBD	C832A7F1	9 5 2	D5C7AAC4	216 5107
68 4 0 7	9F081BF4	7651EDD2	82FD16C1	504EFA8D	4 2019	4DBBF7B8	9170
1C70D433	E80C2F20	0185D936	F5292215	279 59	0336354	3A6FC36C	CEC3387F
F808F18A	0 40 99	E5FDFC8F	115107	2 0	374E10F3	DE17E6D5	2ABB1DC6
8FDCC55E	7B703E4D	9229 86	66853378	B436DF34	409 2427	30201	5D6F2912
17 09822	30 6 31	0 559517	FEF96E04	2 4 8248	D8E6795B	31BF8F7D	513746
6074ACF6	94D857E5	7D81 123	892D5AD0	569 69	AF324D8F	466 9	2 740
D3F4D9C9	275822DA	CE01D4FC	3AAD2FEF	81	1 238 0	F5EBCE96	01473585
A420ED1D	508 160	9 5 028	40791	9FCAF777	6 660 64	823FFA42	76930151
5 061	C8F04B72	21A9BD54	05054647	07 6 0	F31A5118	1 43 73	EEEE5C20
4 8884 5	BF247FA6	56708930	A2D17293	70629EDF	84 65	609793	993668F9

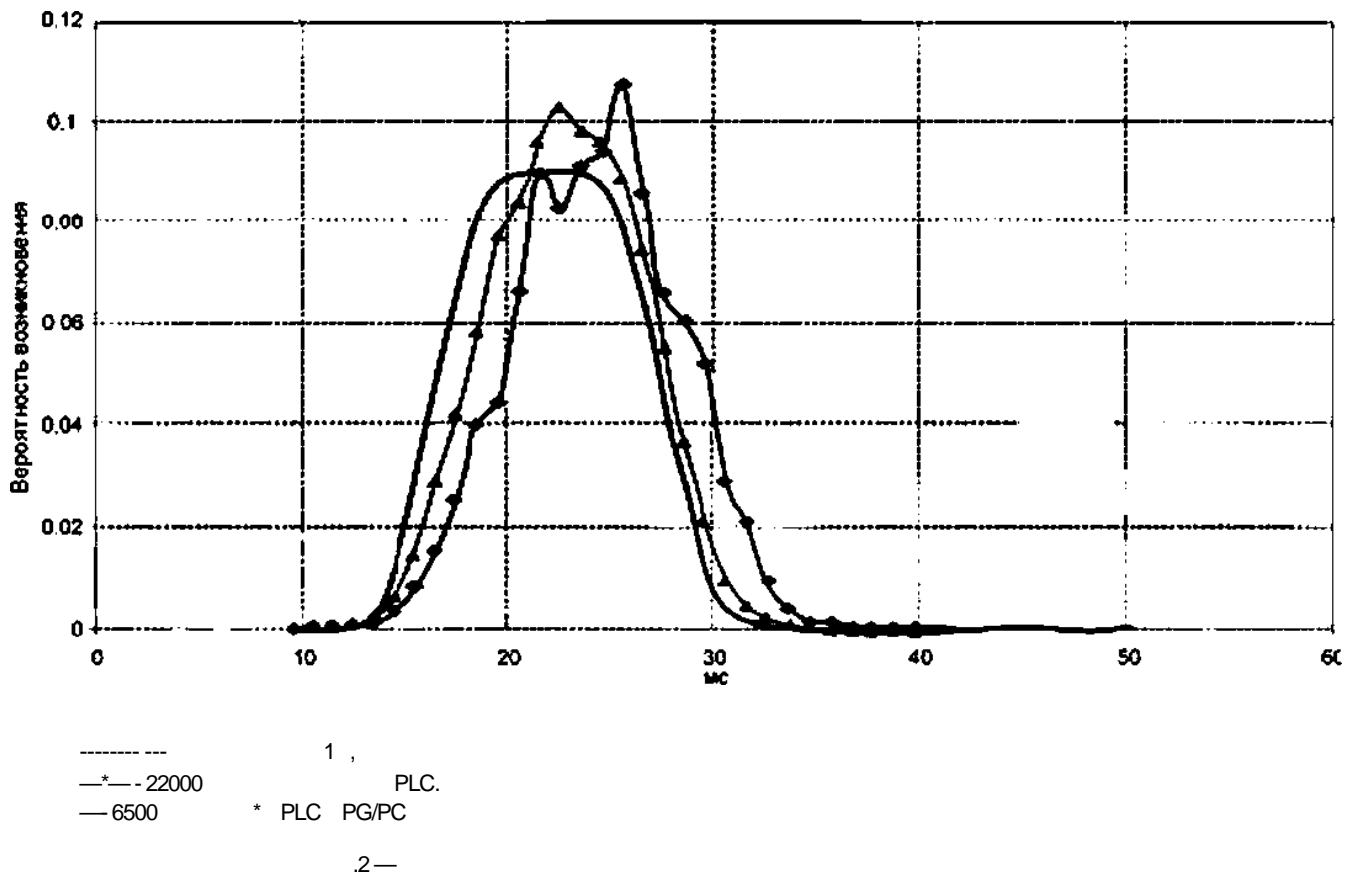
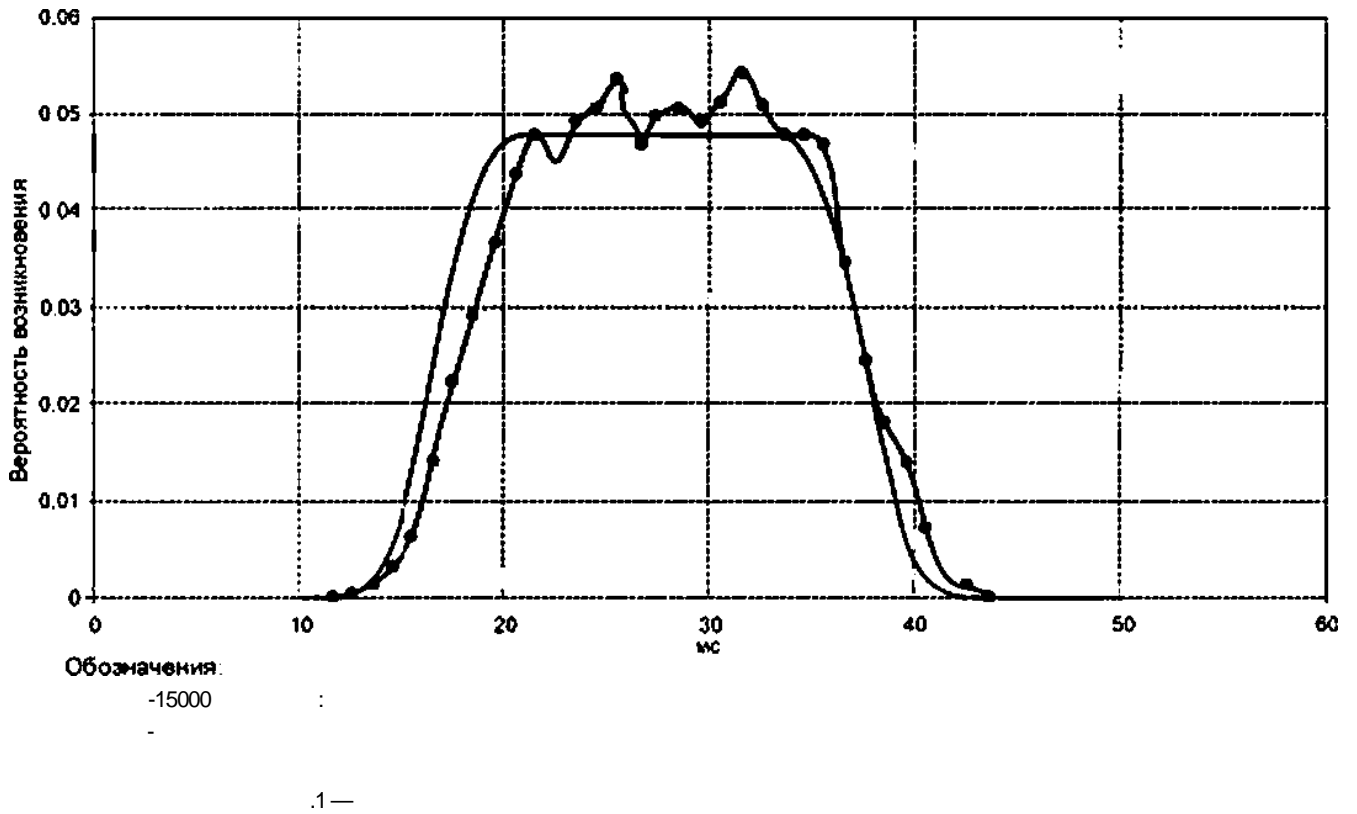
— 32-
 (0.. 255) (32().

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 F-xocr ,
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 3/1 FSCP 3/1, 1.5 /
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 PG PC
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 13 . 35 (24 .)

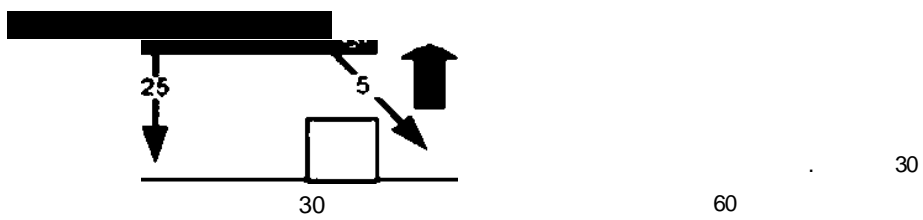
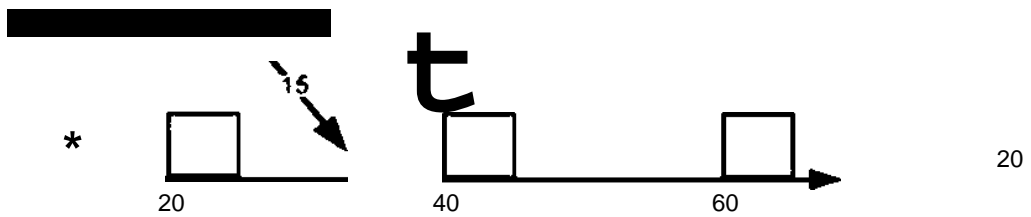
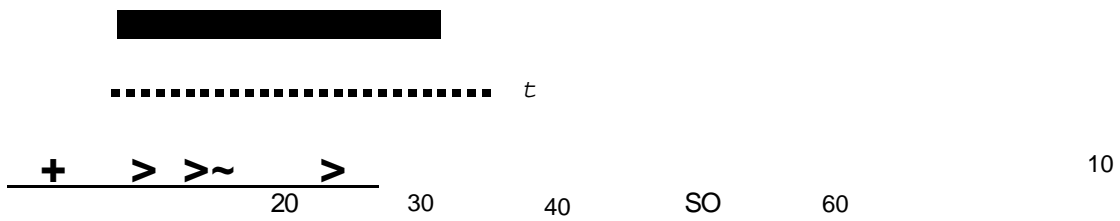
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PLC (CPU):
 — 22 000
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 PLC (F-), () « 2 »
 F- CPU

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CPF 3

FSCP 3/1

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URLwww.profisafe.net

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IEC 60204-1	IDT	60204-1—2007 « 1. » -
IEC 61000-6-2	MOD	51317.6.2—2007 (61000-6-2: 2005) « » - » -
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IEC 61131-3	-	
IEC 61158-2	-	•
IEC 61158-3-3	-	•
IEC 61158-4-3	-	•
IEC 61158-5-3	-	•
IEC 61158-5-10	-	•
IEC 61158-6-3	-	•
IEC 61158-6-10	-	•
IEC 61326-3-1	-	
IEC 61326-3-2	-	
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IEC 61784-2	-	
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